

20/10/2014

Digital Communication

What is the digital communication?

Conversion of analog signal into digital
Transmission of the digital signal

Why digital signal?

Because It have more advantage
coding-decoding
low noise

Low distortion

Low interference

we can say continuous signal
into discrete signal OR
to convert analog signal
into digital

there are three stages to
conversion :-

1. Sampling

2. Quantization

3. Coding : conversion into digital

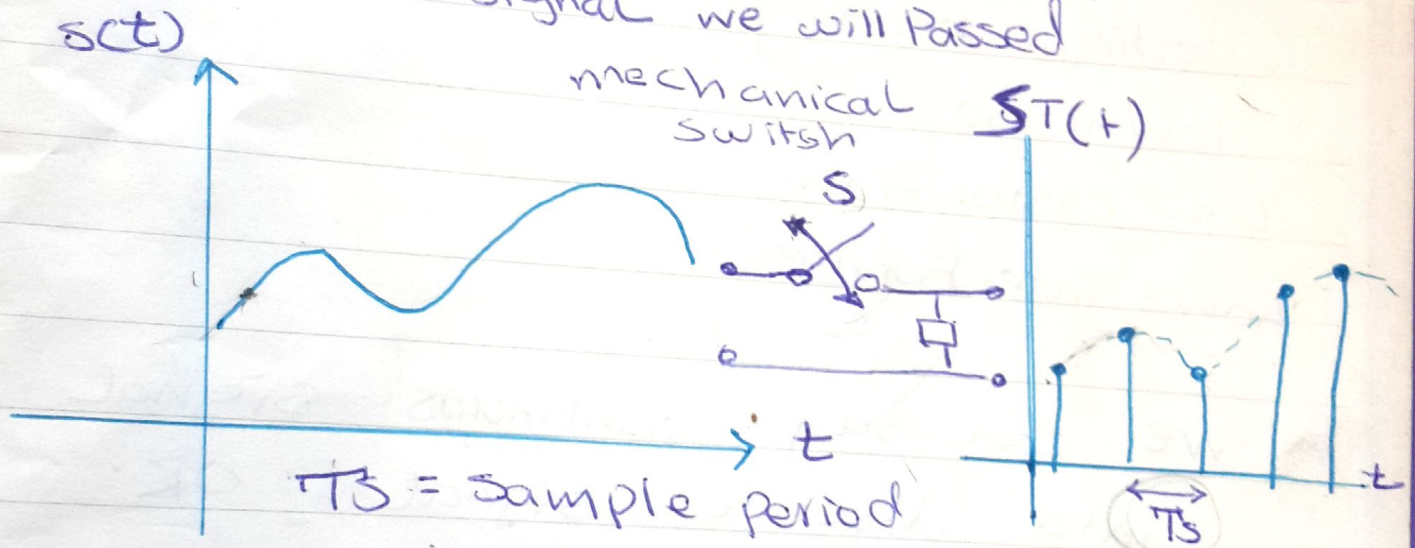
* Quantization

to divid the signal into number of levels.

- Sampling

أخذ العينات = taken samples from the signal
 - أخذ عينات من إشارة مستمرة وتحويلها إلى
 إشارة منفصلة =

continuous signal we will Passed

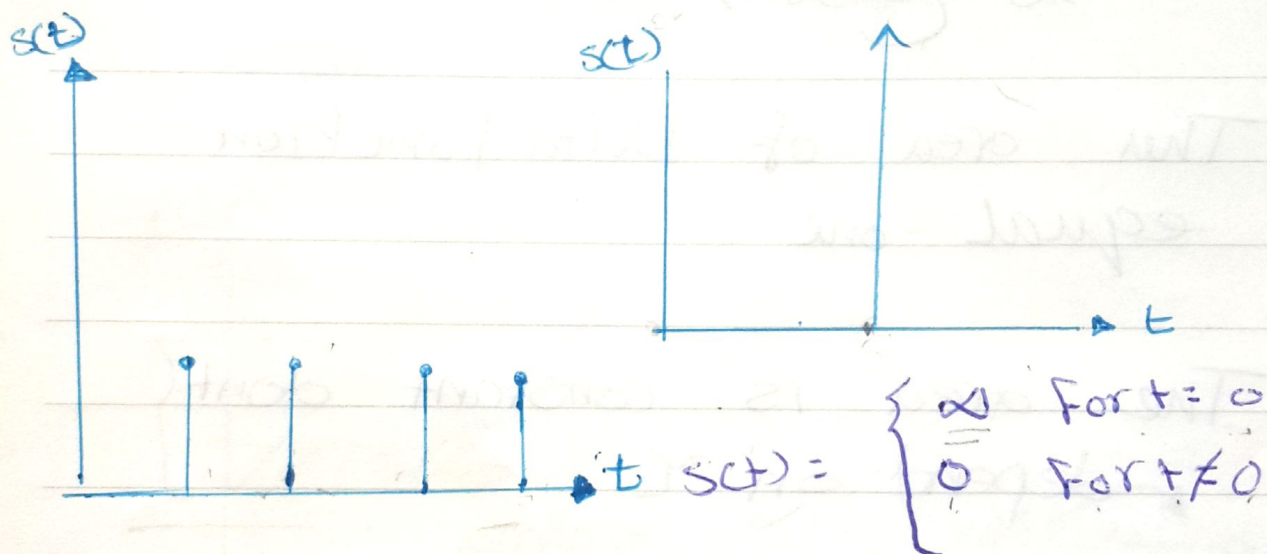


يكون المفتاح مفتوح ومغلق لزمان محدد
 لذلك تلاحظ ان الإشارة تكون
 متقطعة

بدل ارسال الإشارة كلها نرسل عينة
 1. متقطعة

2. ويكون للمينة نفس مستوى القوتية
 3. في الزمن المحدد

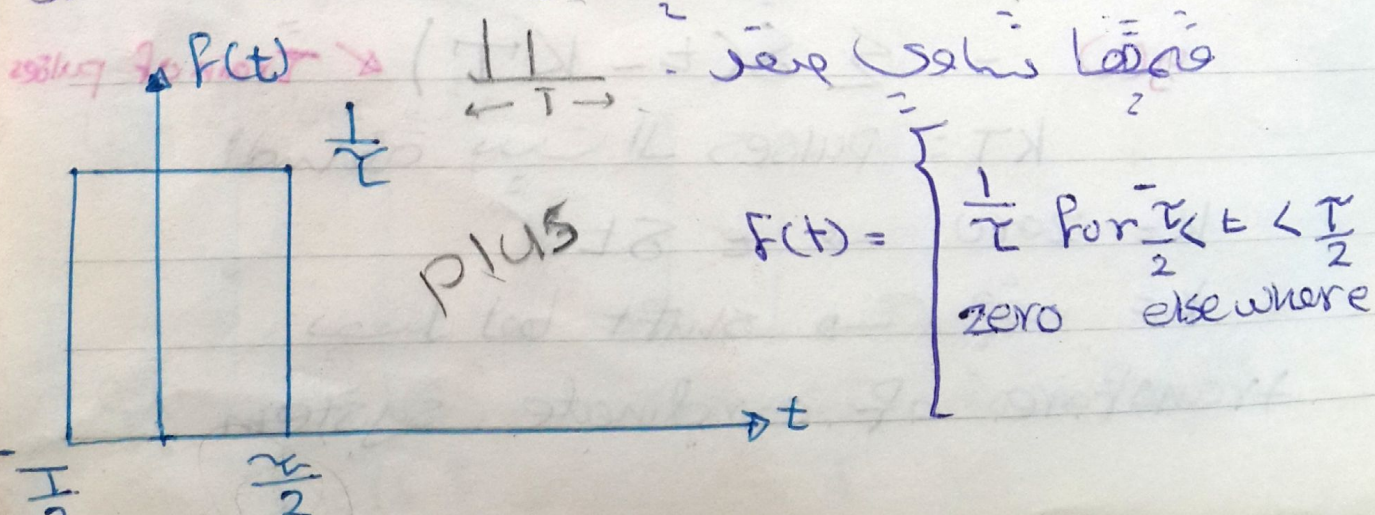
سنة ذات المفتاح الإلكتروني البعدي
لذلك نستخدم المفتاح الرقمي digital switch



Delta Function δ -

which has infinite voltage
when at a time equal zero
otherwise it equal zero

إذا حولنا الزمن إلى T تكون لدينا فترة
عند الزمن المحول وفق الزمان t تكون



impuls $\delta(t)$ \equiv samples
 NO. $\delta(t)$
 DATE \equiv

Another function -

$$\delta(t) = \lim_{\gamma \rightarrow 0} F(t)$$

Delta fun \equiv impulses

∞ الارتفاع , zero العرض

The area of Delta function equal one

The area is constant don't depend on γ

$\int_{-\infty}^{\infty} \delta(t) dt = 1$ = Area

the area under integration or curve.

$$\int_{-\infty}^{\infty} \delta(t) \cdot dt = 1$$

Delta Fun \rightarrow $\delta(t)$ \rightarrow impulses

$y(t) = \sum \delta(t - kT)$ \leftarrow Train of pulses

kT : pulses \rightarrow $\delta(t)$

$k = \text{zero} \rightarrow \delta(t)$

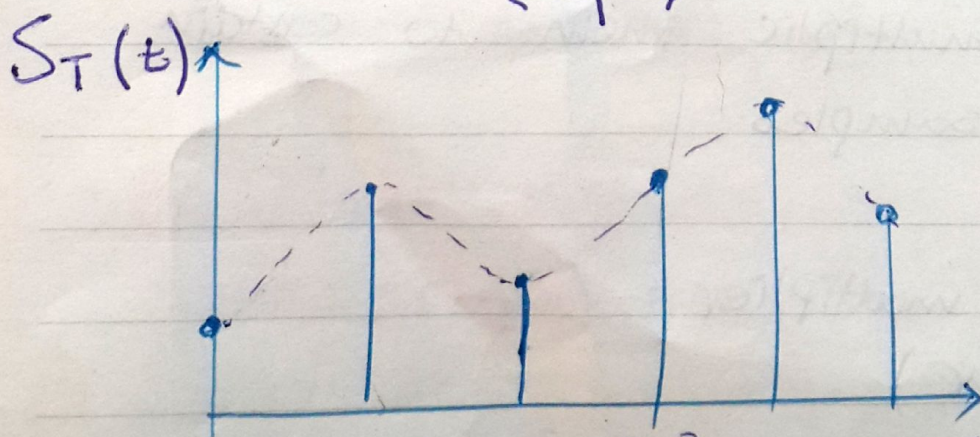
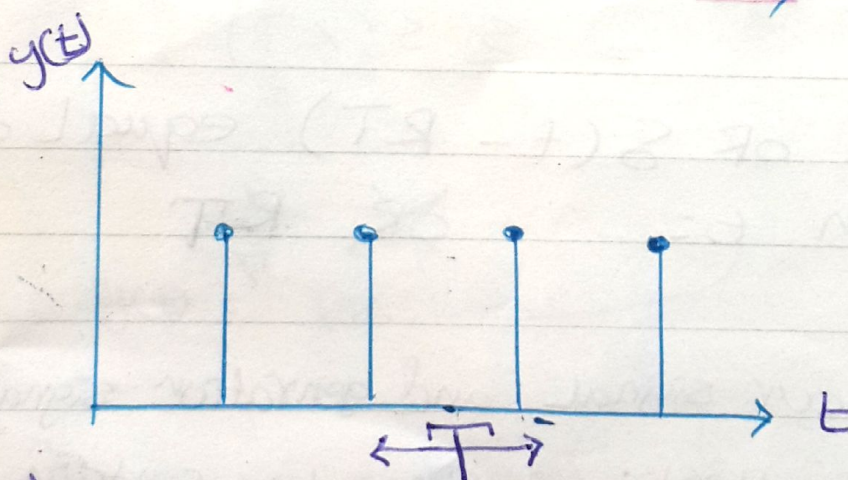
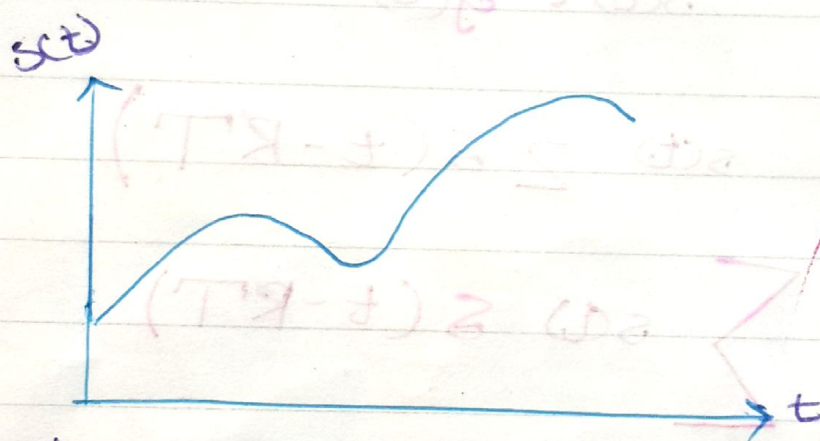
$k = \text{shift} \rightarrow$ shift

transfare of cardinate system ??

* impulses :-

the limit of pulse when $\tau = 0$

train of



level 1 $\bar{a} \bar{a} \bar{a} \bar{a} \bar{a} \bar{a}$
 Delta Function = 1

Transistor
 القاطع ووالذي يقطع الجهد المستمر
 إلى أجزاء متساوية

Mathematical expression of sampling

$$s_T(t) = s(t) \cdot g(t)$$

$$s_T(t) = s(t) \sum \delta(t - kT)$$

$$\underbrace{s_T(t)}_{\text{Samples}} = \sum s(t) \delta(t - kT)$$

$$= \sum s(kT)$$

the level of $\delta(t - kT)$ equal one
 when $t = \dots kT$

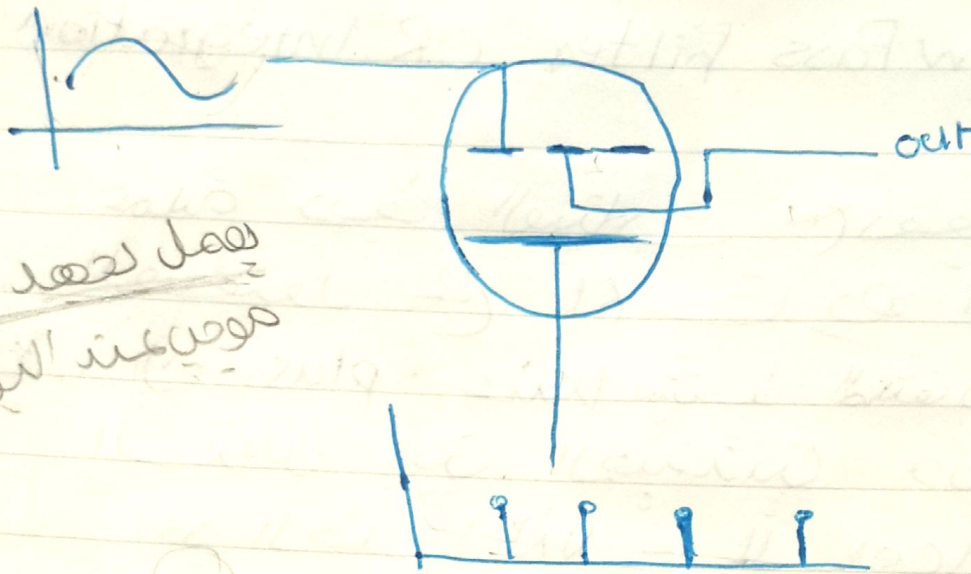
we have our signal and generation signal of $s(t)$ and multiply them to contain the samples.

- Transistor multiplier & (Sampler)

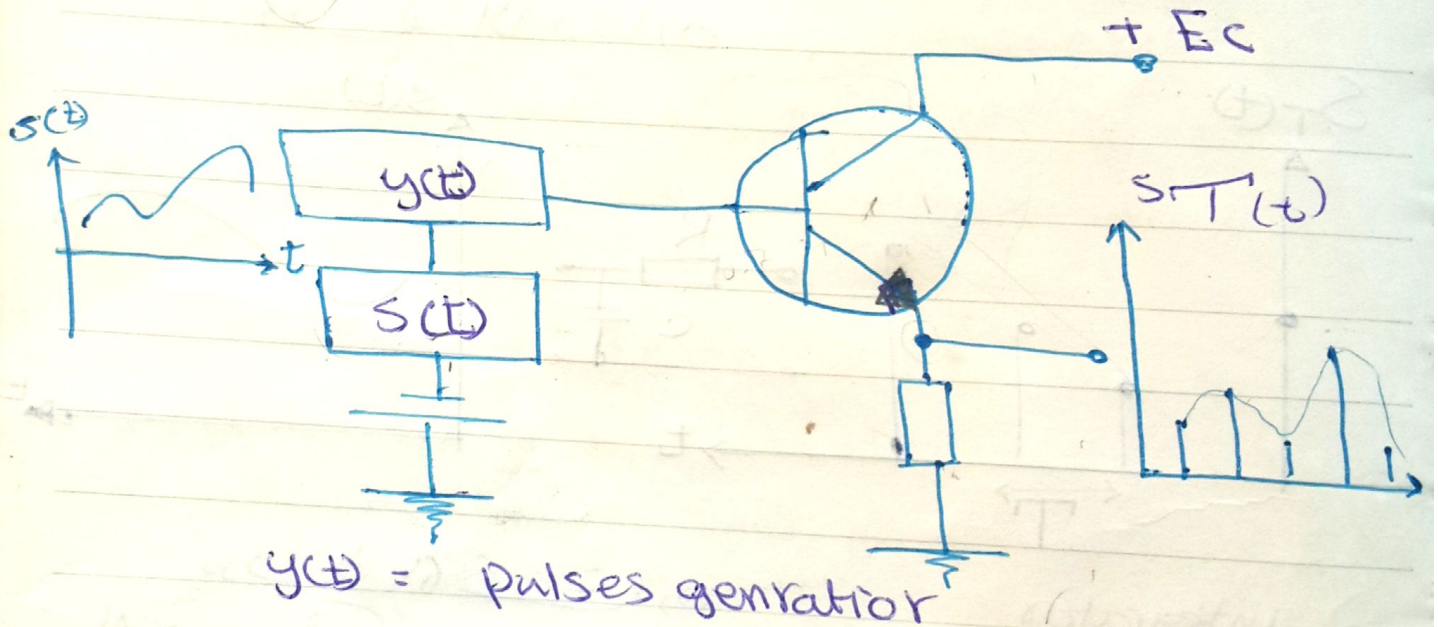
MOSFET :- Enchancement

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يعمل كحجم
موسعة النواقل



$y(t)$ = pulses generator

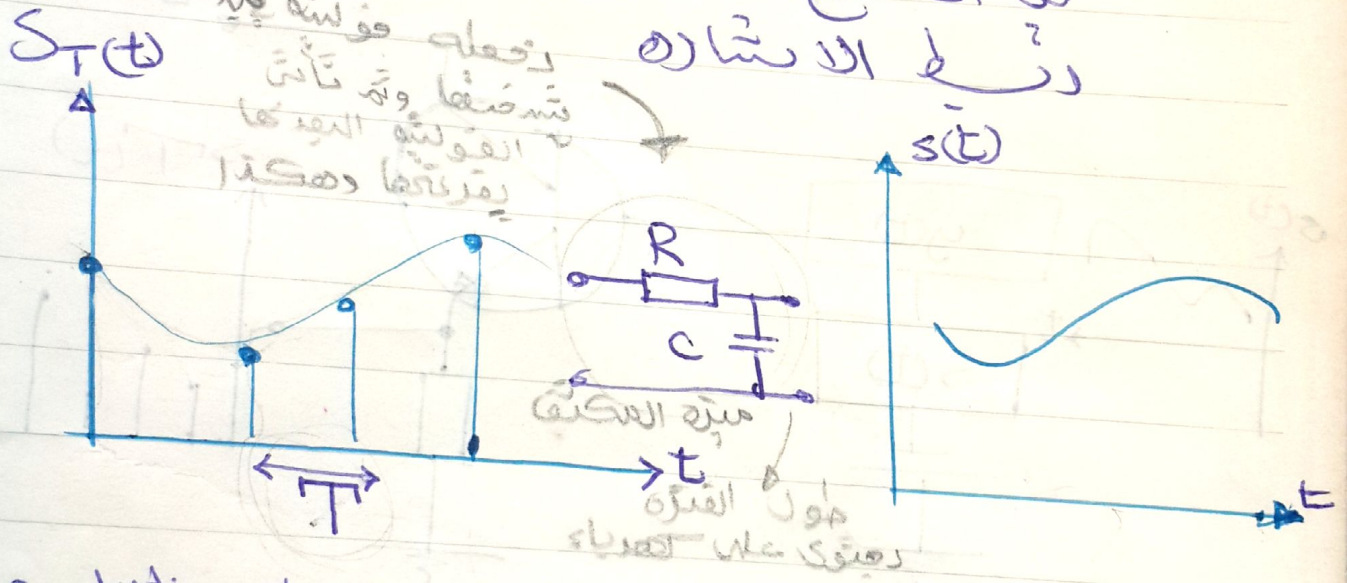
نوع التجميع E_c من
نوع الدارة common collector

how to restruct or ditfiction
the original signal?

كيفية استرجاع الإشارة الأصلية

Low Pass Filter OR Integration

عملية دمج النبضات مع زخميتها البقية
للوصول على $A(t)$ في الأصل
ال plus على $u(t)$ بسحب المتكامل
الزمن بين العينتين و $discharge$
في الخرج لذلك $capacitor$ مدمج
في الإشارة



• Integrators

• Low Pass

التي تكون من عدة ترددات
لومر أقل تردد يبقى في إشارة الأصلية

what is the length of the period
(T) ?

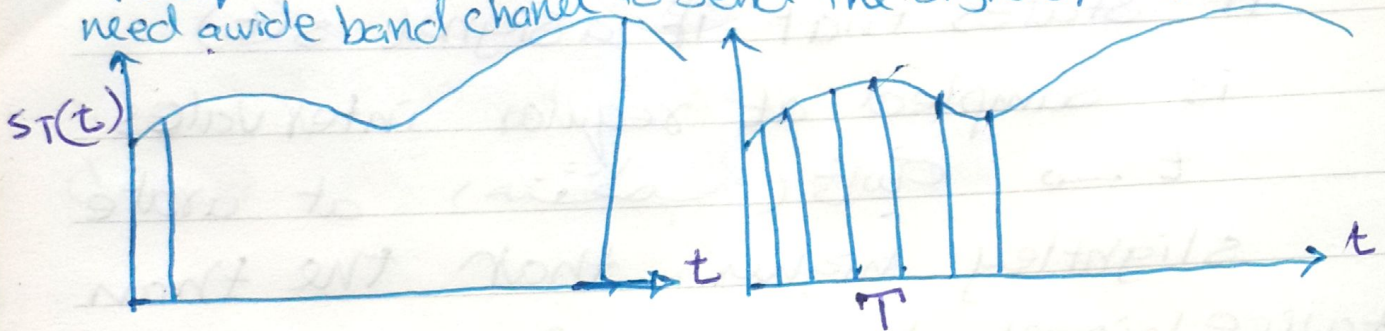
إذا زدت T إلى ∞ فستصبح أرباع الإشارة

If T is long the original signal

can not be restruct

very high $\leftarrow T$ فاصل زمني كبير

need wide band channel to send the signal, high cost.



لذلك لابد من عدم تجاوز حد اختيار

T كبيره او قليلة حتى نشترك من

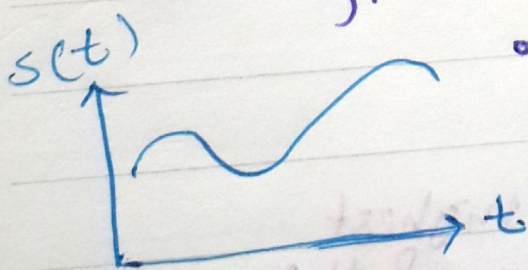
استيعاب الإشارة الأصلية كاملة

compromise between two

what is the optimum T ? ^{مثالي}

مبدأ أخذ العينات sampling principle

nyquist principle



the sampling Principle

specifies the least number of samples of a signal for its complete definition

* nyquist ^{قاعدة}

حد أدنى أقل عدد

من العينات لتقدير الإشارة تقريب كامل

cycle NO.
 2 sample

قال بقوله المصنف رحمه الله
 ما قاله تاتت اقل من واحد

It states that if a signal $s(t)$ is sampled at regular intervals $t \rightarrow$ (تأثير) at a rate slightly higher than the twice highest frequency of the signal. Then, These samples contain all the information of the signal.

لو كانت أخذ العينات بسرعة أكبر
 بقليل أكبر من ضعف أعلى إشارة
 في هذه الحالة نتحصل على إشارة تمثيلية
 كل المعلومات (المعلومات)

$$R_s \geq 2 f_n$$

the sampling rate sample/sec \swarrow \nwarrow the highest frequency of the signal

sps f_s = samples/sec في الثانية

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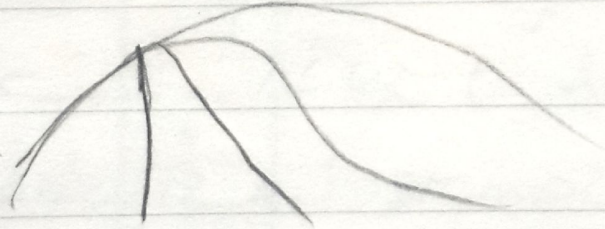
DATE

* تردد التلغون 3400 Hz

* الاتحاد الدولي للاتصالات زاد التردد Rs
8000 بيل 6800

ITU regulation

* كلما زاد عدد القنوات كان اقصد

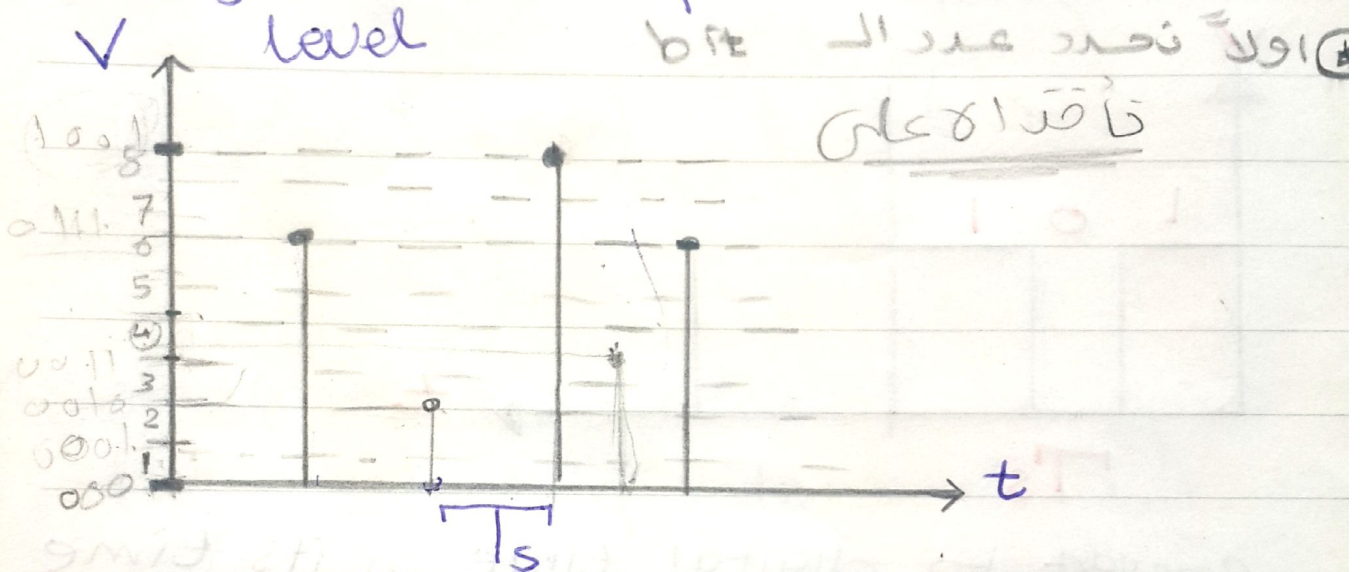


* Quantization :-

* to divide the signal into number of levels

Approximation each sample to a given number of levels (volt).

to map each sample and to record level



أي أنه في هذه الحالة، كل مستوى من المستويات يتم تعيينه كود رقمي

بما أن مستوى التمثيل هو 2

coding

* num of levels :-

2, 4, 8, 16, 32, 64, ---, 256

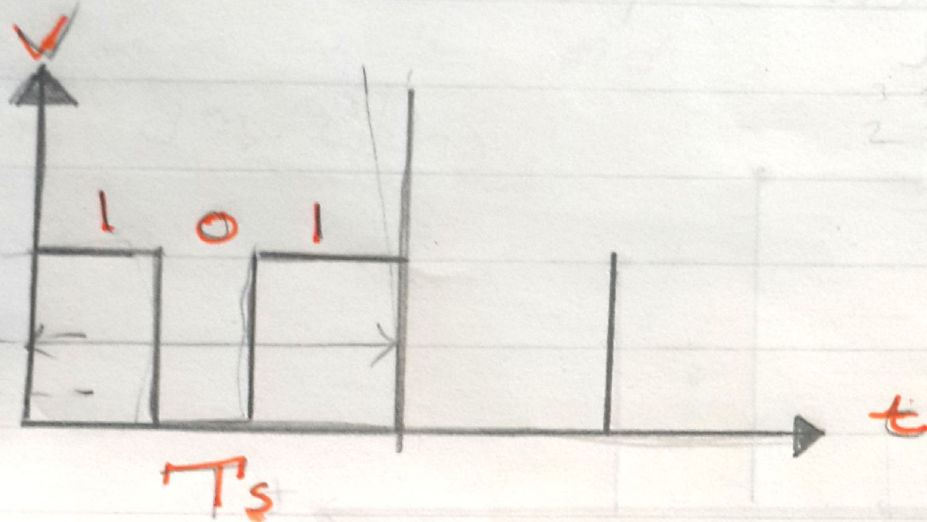
N : the num of levels

we can convert the sample into

a digital word
the process after

PCM Puls code modulation

5V \rightarrow 10000000
 00101 \rightarrow 00101



convert to digital time in its time

each word have its length or
 have a given number it call bits

IF the number of bit is Three
 we will divide it: 101

the number of bit depend on
 the number of level

1 volt \rightarrow 3 bit

100 \rightarrow 200 bit

num
of
levels

$N = 2^n$
 N → num of levels
 n → num of bits
 binary

bits 11 10 01 128 levels 11 10 01 131
 2 1 5 8 9 7

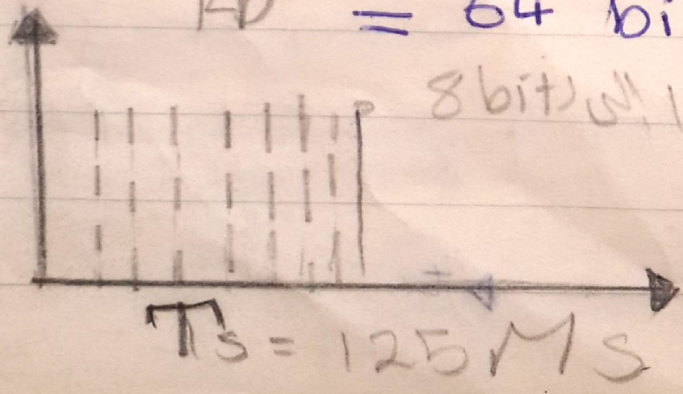
1 → 1

what is the relation between the sampling rate R_s and bit rate R_b

$R_s \times n = R_b$
 8000 × 8 = 64000 bits/sec

If the number of level of our the telephone signal is 256 calculate the number of bits (bit rate of a signal)?
 bit rates number of bit/sec

$n=8$ $R_b = 64 \text{ bit/sec}$



8 bit / 125 μs
 $= \frac{125 \mu\text{sec}}{8}$

$$R_s \times n = R_b$$

$$F_s = \frac{R}{\log_2(N)}$$

$$R = F_s \log_2(N)$$

$$T_b = 125/8$$

$$\frac{1}{\frac{125}{8}} = \frac{8 \times 8 \times 10^6}{125 \times 8} = \underline{\underline{64 \text{ kbit/sec}}}$$

$$\frac{1}{\text{الزمن}} = \text{التردد (bit rate)}$$

$$\text{Sampling rate} = 8000$$

$$\frac{1 \text{ sec}}{8000} = 125 \text{ Msec} = \text{المدة}$$

R_b نقسم على 8 فانا نساقي R_b

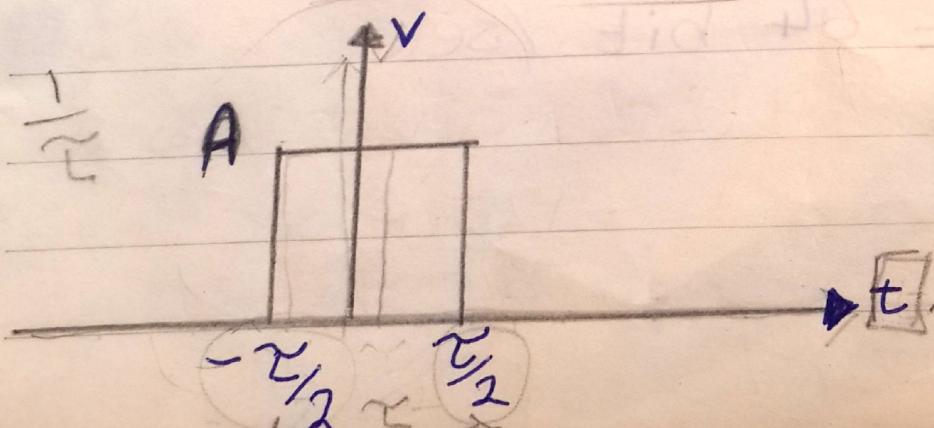
$$* R_s = \frac{1}{T_s} \quad , \quad R_b = \frac{1}{T_b}$$

$$256 = 2^n \rightarrow n = 8 \text{ bits}$$

$R_B = \text{Baud rate} \rightarrow \text{symbol/sec}$

the number of symbol changes

Band width of a pulse



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the digital signal its a pulse
we try to find the width
of this pulse.

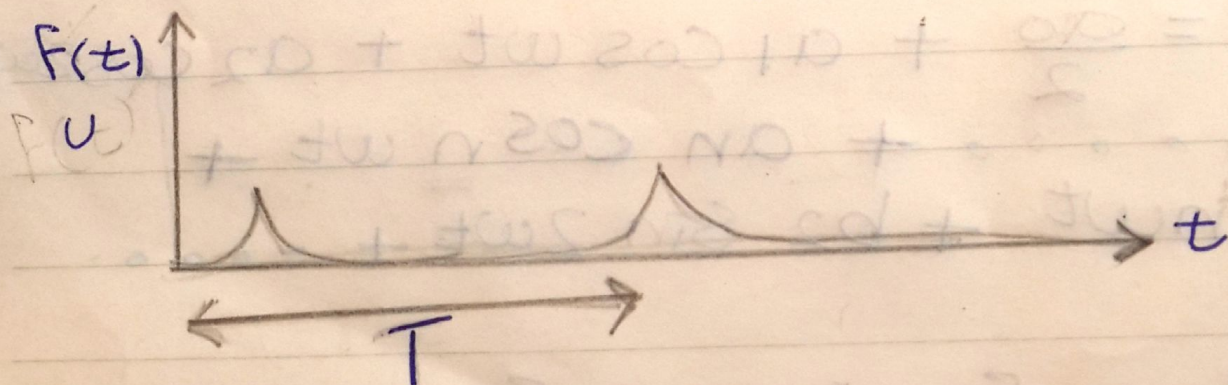
the digital signal have
very wide band width

what is the Fourier Series ?
what is the Fourier Transform ?

CF 171 * FS :-

If we have aperiodic signal
this signal can be expended
into a number of orthogonal
signals.

اذا كان لدينا اشار (دوريه) فانها
يمكن تحويلها الى اشار (غير متعامده)
(dot Product = zero)



الاشارة المتعامدة :-
 $(\sin x, \cos x, e^{\pm jx})$

$\varphi_1(t), \varphi_2(t), \varphi_3(t), \dots, \varphi_n(t)$
 كل صندب اي 2 صندب = zero

• نتون هذه النوال متعامدة اذا كانت ال
 Zero Dot Product (شرط التعمد)

$$\int_t \varphi_i(t) \varphi_j(t) = \begin{cases} 0 & \text{if } i \neq j \\ K & \text{if } i = j \end{cases}$$

تقريباً Orthogonality

Par seval

$$\int \sin x + \cos x = \text{zero}$$

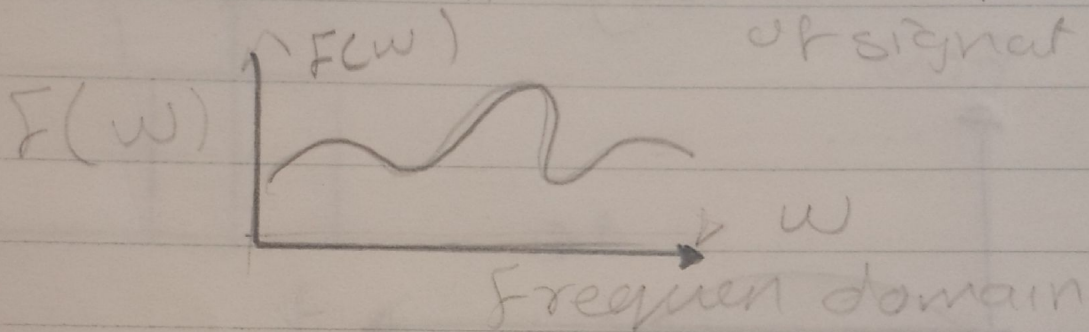
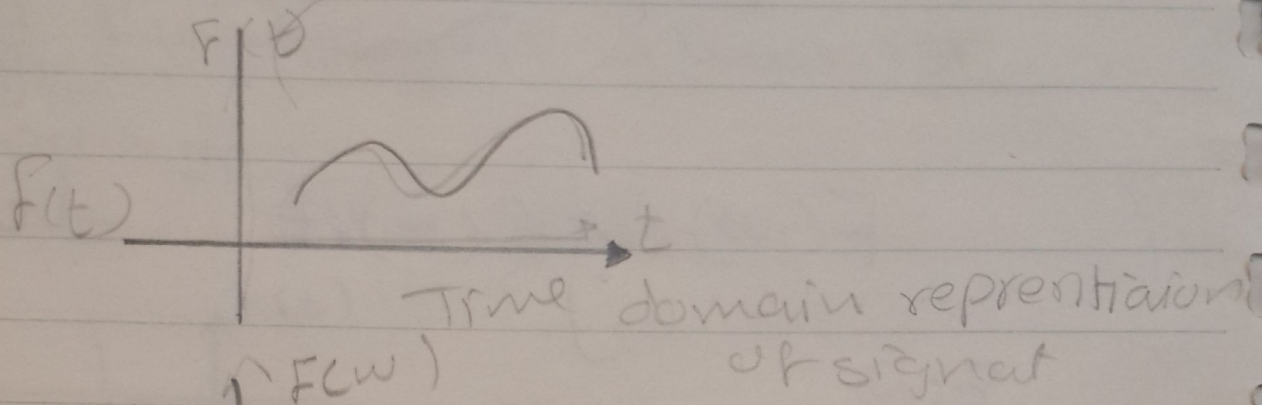
Fourier نظرية *

$$f(t) = \frac{a_0}{2} + a_1 \cos \omega t + a_2 \cos 2\omega t + \dots + a_n \cos n \omega t + b_1 \sin \omega t + b_2 \sin 2\omega t + \dots$$

a, b = Fourier coefficients

F. series:-

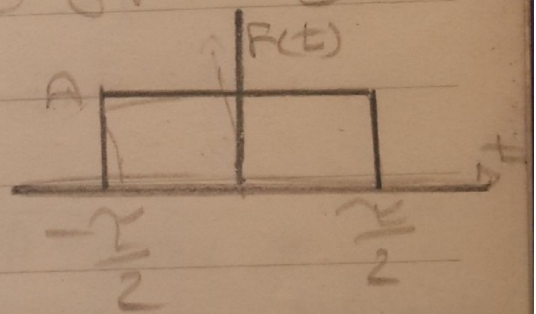
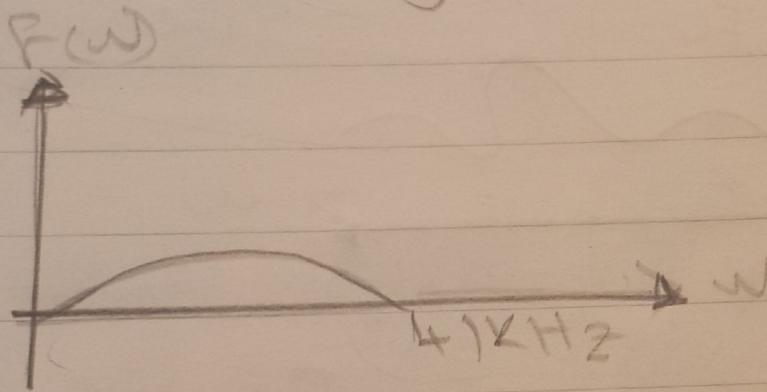
$$f(t) = \frac{a_0}{2} + \sum a_n \cos n\omega t + b_n \sin n\omega t$$



أي دالة أو إشارة ذات شكل دوري يمكن
أن تكون مجموعاً من إشارات جيبية
بالتكرار $\omega, 2\omega, \dots$
 ω Frequency

F - T $\rightarrow \omega = 2\pi f$

المتلازمة من التردد في التردد
المتلازمة من التردد في التردد
في التردد في التردد
في التردد في التردد
4 KHz



$$F(\omega) = \int_{-\infty}^{\infty} \underline{A} e^{-j\omega t} dt$$

$$\frac{0}{0} = 1 \rightarrow$$

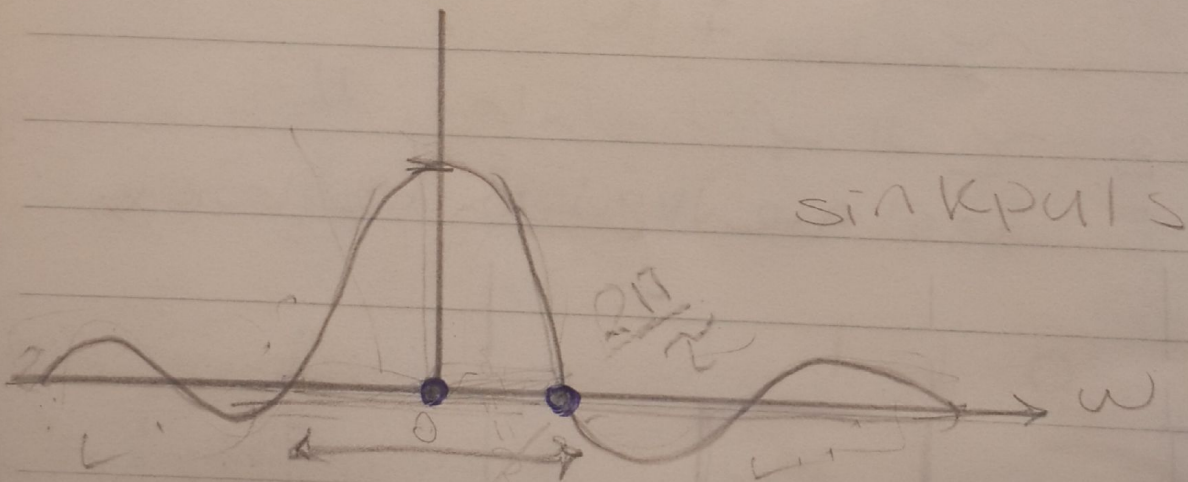
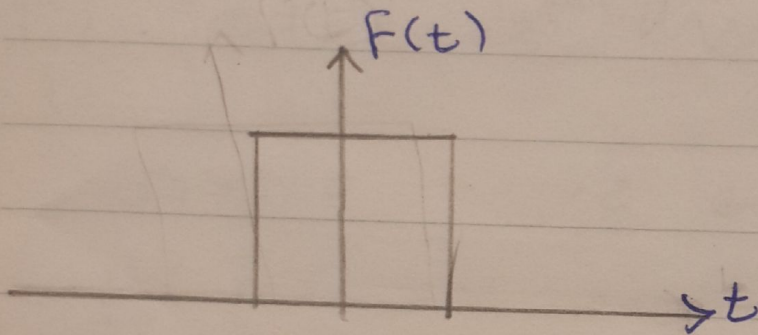
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$$F(\omega) = A \tau \frac{\sin \omega \frac{\tau}{2}}{\omega \frac{\tau}{2}}$$

band width curve & lines 131
width of puls

sin k puls



تأثيرات النطاق من حيز القبة

$F(\omega)$

the maximum of when $\omega = 0$ zero

$$\sin \pi = 0$$

$$\omega \frac{\tau}{2} = \pi$$

$$\sin x$$

$$x$$

sin function

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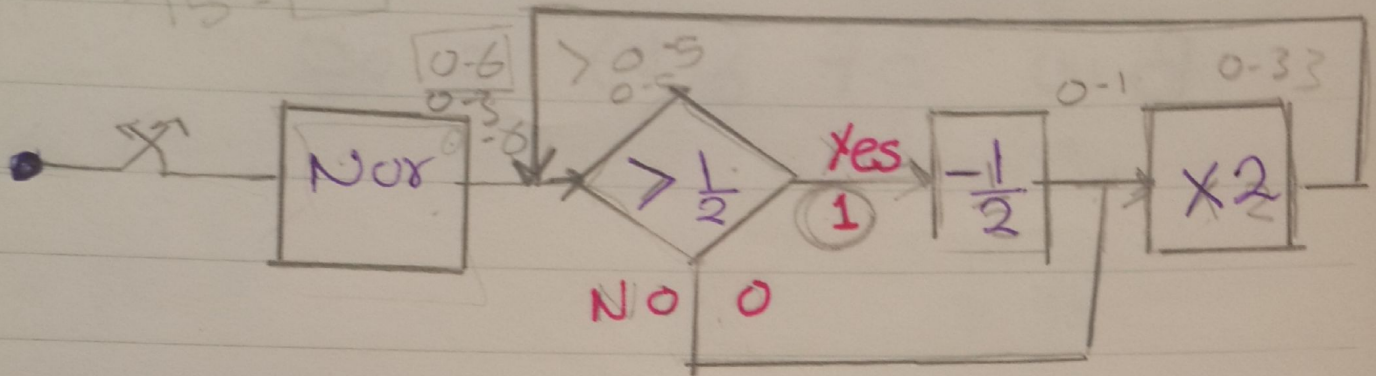
$$BW = \frac{1}{T}$$

$BW \rightarrow \infty$ when $T=0$

$$BW \text{ of } \delta(t) = \infty$$

Coding (Example)

* Serial Encoder :-



* Normalizing means dividing by the max level

e.g. 10V, 3V, 5V, --- 15V levels
Each sample to divide by 15.

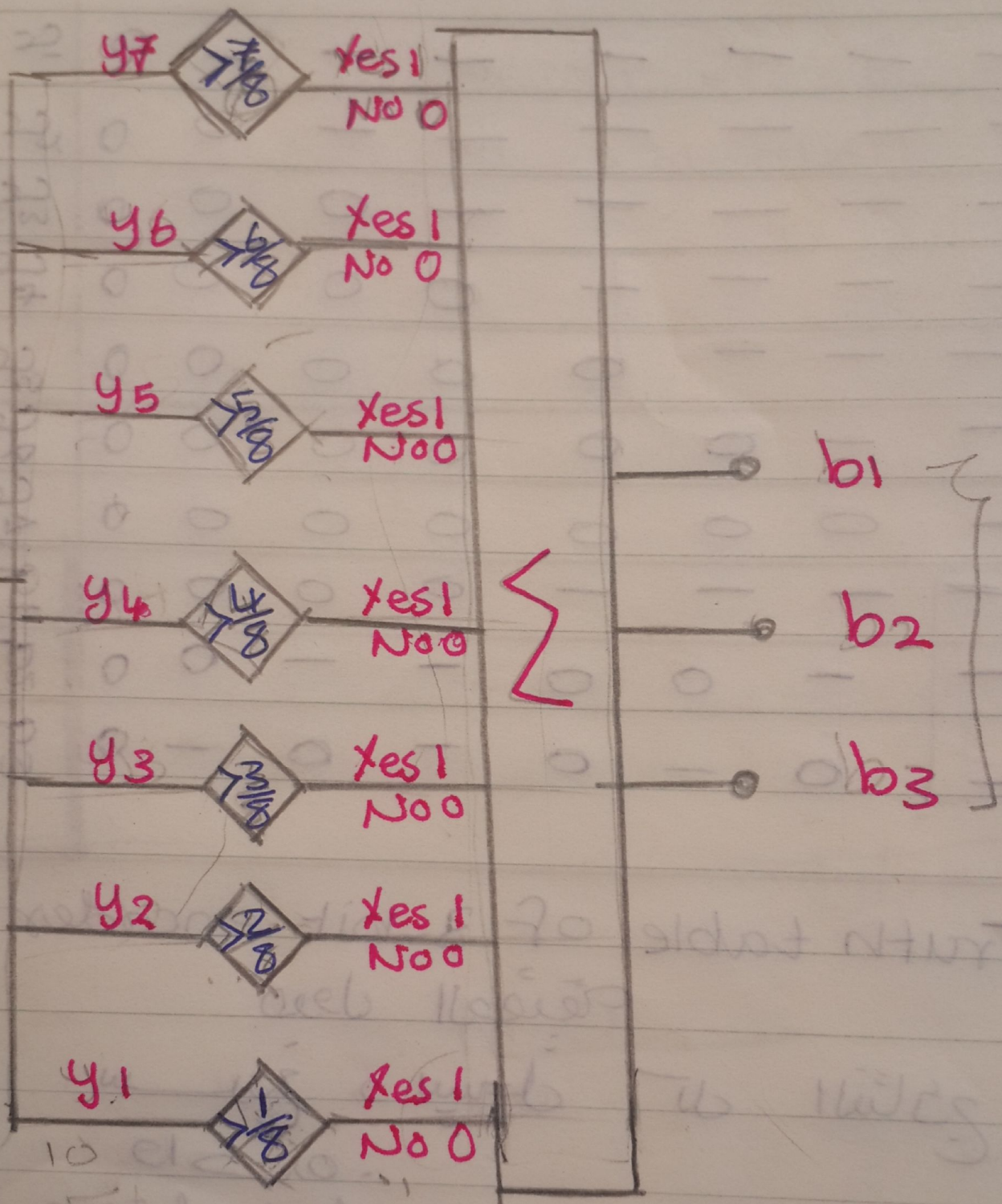
The number of the bits at the output depend on the number of level.

Nor:- Normalizing \rightarrow maximum of levels equal 1

$> \frac{1}{2}$: comparator record one or each number zero

$-\frac{1}{2}$: subtractor

maxiam, num of bsr



3 bit - encoder
Parallel

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0	1	1	1	1	1	1	0	y ₁
0	0	1	1	1	1	0	0	y ₂
0	0	0	1	1	0	0	0	y ₃
0	0	0	0	1	0	0	0	y ₄
0	0	0	0	0	0	0	0	y ₅
0	0	0	0	0	0	0	1	y ₆
0	0	0	0	0	0	1	0	y ₇
0	0	0	0	0	1	0	0	b ₁
0	0	1	0	0	0	0	0	b ₂
0	1	0	0	0	0	0	0	b ₃

Truth table of 3-bit encoder
 جدول الحقيقة

سريع و بسيط
 النتائج في لحظة

pulse code modulation

ماتریسی ثقلی $\rightarrow \frac{1}{R_T} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \dots$

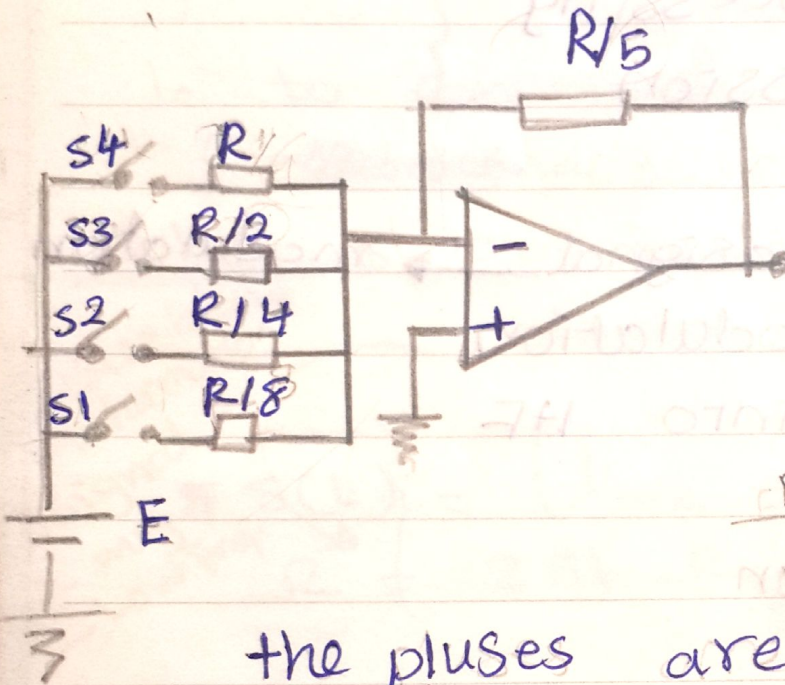
DAC

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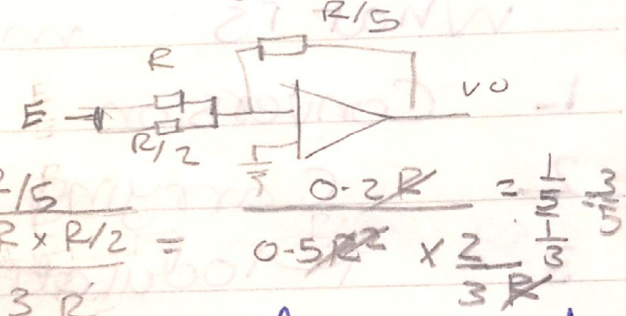
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An Example of 4bit Decoder
Detector

reconstructor



$$V_{O1} = -\frac{R_F}{R_i} \times \frac{R/5}{R} = -\frac{1}{5} \times E = -E/5$$



the pluses are applied through
this switches

switch \rightarrow ON \rightarrow 1
switch \rightarrow OFF \rightarrow 0

S1	S2	S3	S4	O/p
0	0	0	0	0
0	0	0	1	E/5
0	0	1	0	2E/5
0	0	1	1	3E/5
0	1	0	0	4E/5
0	1	0	1	8E/5
1	0	0	0	15E/5

EXG = Out
G1 = $\frac{R_F}{R_i}$

- 1- Signal Processing
- 2- Transmission

* to transmit a signal \rightarrow modulation
what is modulation

- 1- Conversion into HF
- 2- Carrying
- 3- Modulation

we want to transmit our msg
we first convert signal into
digital (take sample, " " " ")

بعد تحويل الإشارة إلى digital
التي هي Transmission

to transmit the signal, or the
information or the base band
it's relatively low frequency
If we transmitted directly it will
be affected by interference
or distortion or attenuation, ---

* كلما زاد التردد زاد الطاقة وبالتالي نحتاج طاقة أكبر

The First step before transmission

1. to converted into high frequency domain

what is mean by high frequency domain?

Baseband
Information
Signal
msg

* $s(t) = U \cos \omega t$
Low Freq $\omega = 2\pi f$

تردد زاوي

Wave
Carrier
High Freq

* $U = U_0 \cos \omega t$

$\omega = 2\pi f$

modulation:-

The Process carrying the low frequency (signal) by high frequency (wave)

Changing the Parameter of the high Freq by the low Freq

تحويل المعلومات الى كود كهرمغناطيسي لنقلها
مثلا ام رسال ولذلك لابد من تحويلها
الى نطاق التردد العالي (حمل المعلومات)
بواسطة التردد العالي - التردد

Amپ على حسب
المستارة

الحمد لله
للحالة

بردد
طهر
NO
کتاب
DATE

مَبْدُوءُ تَقْوِيَةٍ

التقدير: التقدير حتى أحد مدخلات
الإشارة ^{العامة} بواسطة ^{الرسالة} ^{العمومية}

$$u = u_0 \cos \varphi$$

$$u = U_0 \cos(\omega t + \theta)$$

angular
Freq

التشفير هو أحد أنواع الإشارة عن
طريقته modulation تشفير

we have three types of modulation:-

Amplitude modulation \bar{a}_{am} $\frac{1}{2} \bar{a}_{\text{am}}$

2. Frequency modulation التردد modulation

3- Phase Amp modulation sebti =

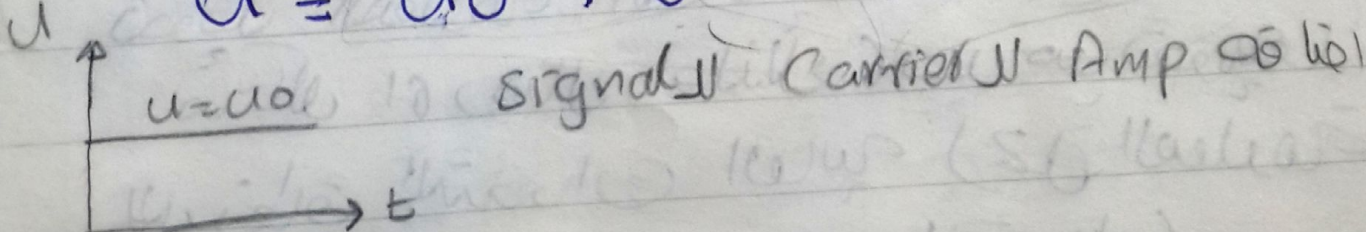
3- Phase variation of the Amp Freq of the wave by signal phase

تفسير سورة العنكبوت
السلامة

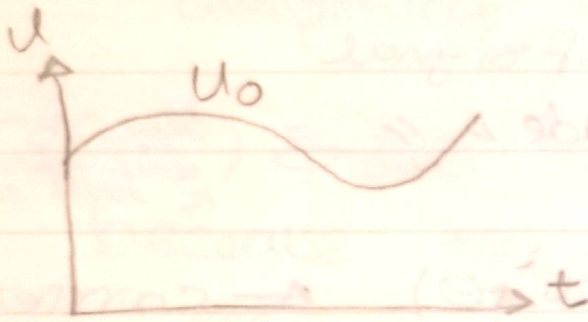
- ★ Amplitude modulation:-

new do

$$u = u_0 + u_1 \cos \omega t$$



maximum $\cos = \pm 1$



U_0 تغيرت و $u = U_0 + U_m \cos \omega t$

$$u = U_0 + U_m \cos \omega t$$

new Amplitude

but we are transmitting digital signal
not analog signal
digital signals have only two level
corresponding to 0 or 1
أو على \pm

لذلك \Rightarrow تصبح المعادلة الجديدة

$$u = U_0 \pm U_m$$

$$u = U_0 \left(1 \pm \frac{U_m}{U_0} \right)$$

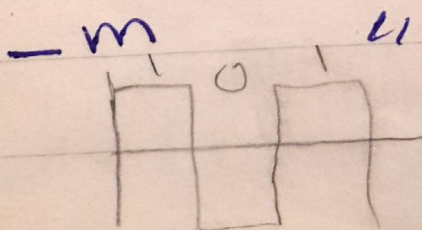
The Process call **digital**

Amplitude modulation (because we have only 2 amp)

$$u = U_0 (1 \pm m)$$

m = modulation index

$\pm m$ when transmitting 1



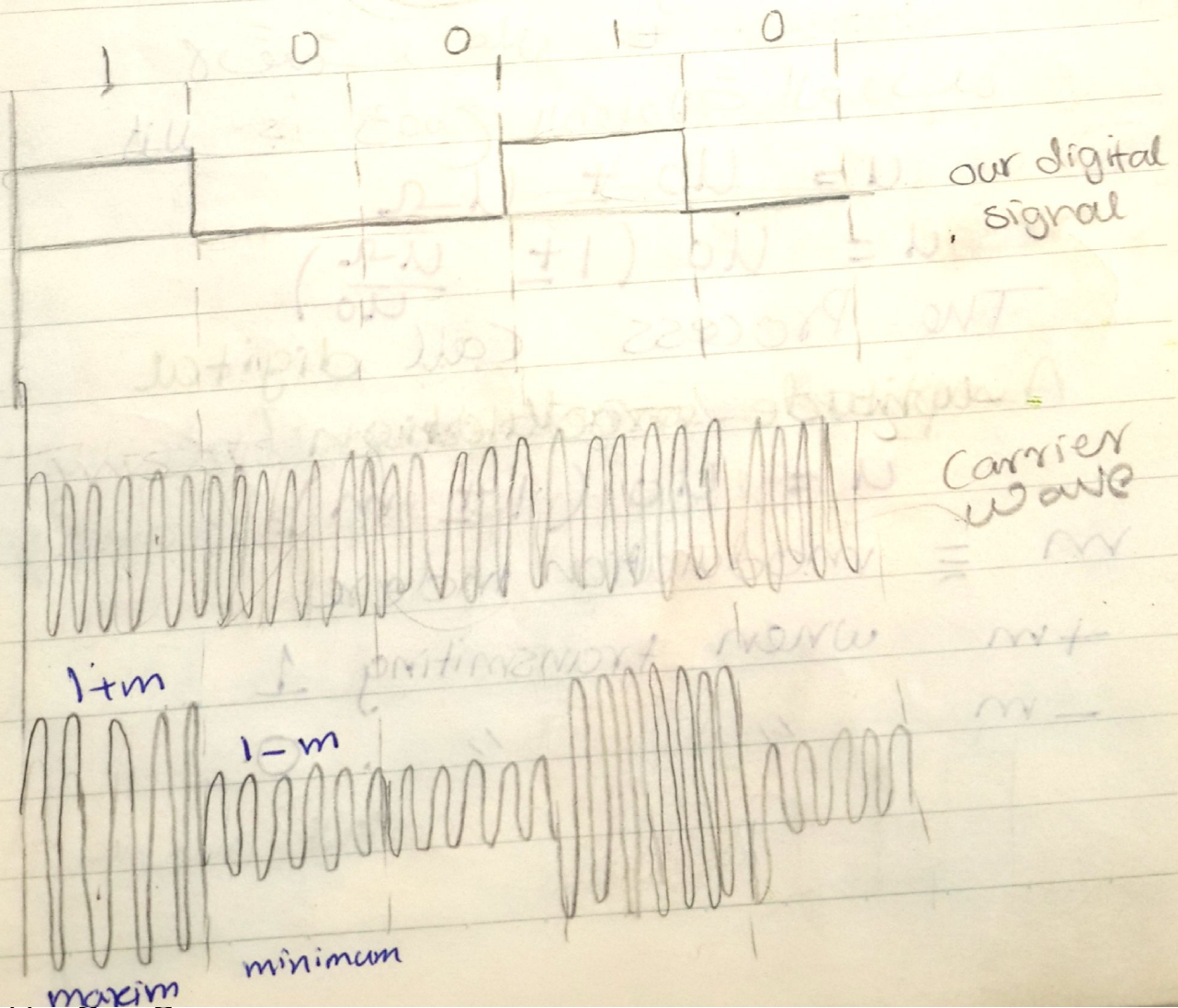
code ال 1 و 0

$U_r \equiv$ the level of signal
the amplitude \ll

$$u = U_0 \cos(\omega t + \theta) \quad \leftarrow \text{carrier}$$

$$\begin{aligned} U_{\max} &= U_0(1+m) \\ U_{\min} &= U_0(1-m) \end{aligned} \quad \left. \begin{array}{l} \text{depend on} \\ \text{the level} \\ \text{of the pulse} \end{array} \right\} \text{given } \rightarrow 2\text{level}(0,1)$$

FSK



FSK
It is also called
amplitude shift keying **ASK**
Lec 5

الإفقال بإزاحة السعة
because the amplitude shift from
one to other
لا تفرق بين سعة
لا تفرق بين سعة
0, 1 level

Am = product modulation

$m=1 \rightarrow$ Full modulation

حتى تكون 10 - 100% (الطاقة - السعة)
واحد

1500K - 1500K - 1500K
قدرة 10, 100, 1000

$\rightarrow P = P_0 \left(1 + \frac{m^2}{2}\right)$ القدرة بعد التعديل

قدرة الناقل
Carrier Power

$m=1 \rightarrow$ القدرة تزيد 1.5

$m=0.5 \rightarrow$ تزداد 0.5

$F_m \rightarrow$ إذا زاد التردد أو انخفضت السعة

استهلاك الطاقة A_m

$m < 1 \rightarrow$ بتغير شكل الإشارة

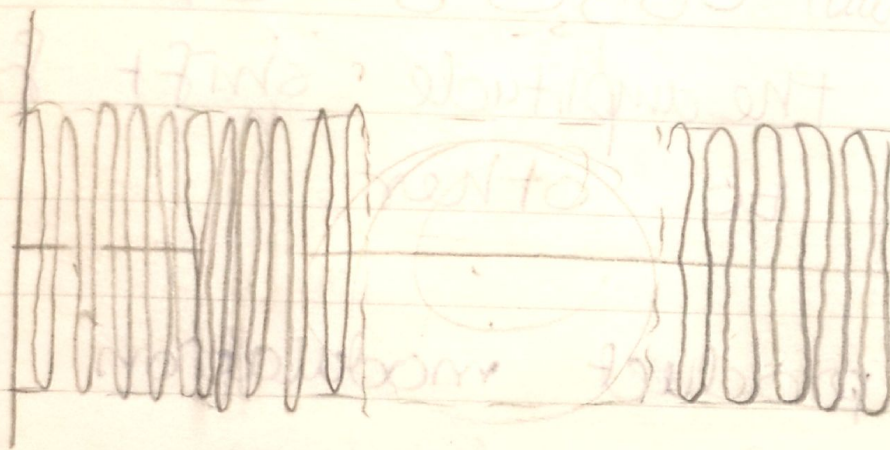
0.95 \rightarrow effective

$m=1 \rightarrow$

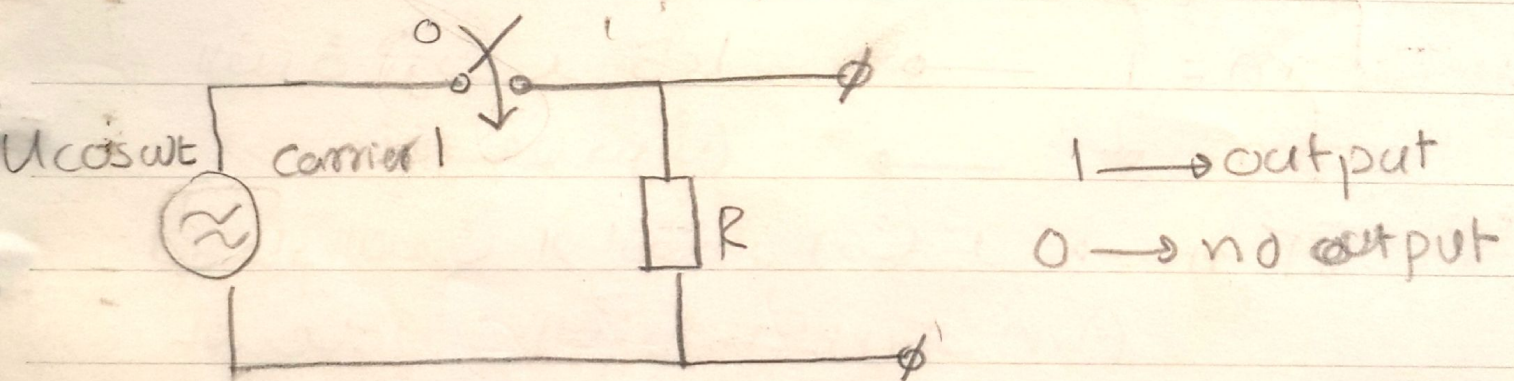
$U_{max} = U_0(1+1) = 2U_0$

$U_{min} = U_0(1-1) = \text{zero}$

but in the case the input signal is zero the result as shown in Figure



ASK
 $m=1 \rightarrow 00K$ ON OFF or 1 0
 Keying
 a simple ~~Am~~ modulator



ASK $m=1 \rightarrow 00K$

$$V_{max} = (1+1)V = 2V$$

$$V_{min} = (1-1)V = 0V$$

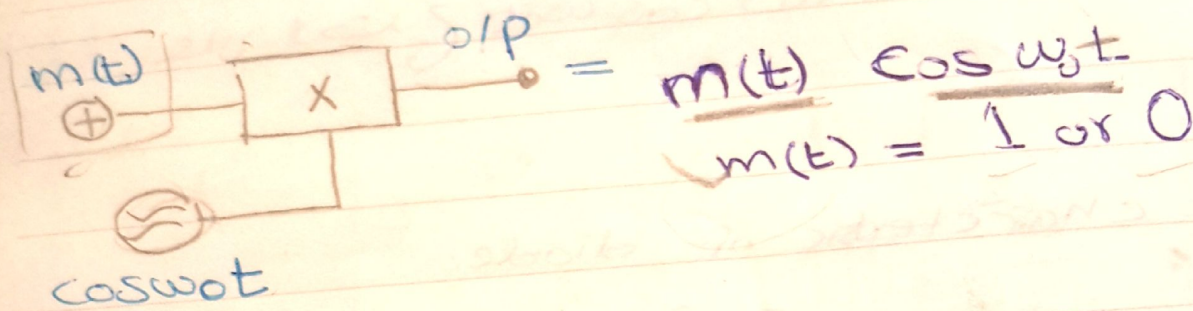
12-2
73
C204

السبيل

NO

Lec 5

ON OF Keying ASK modulators -

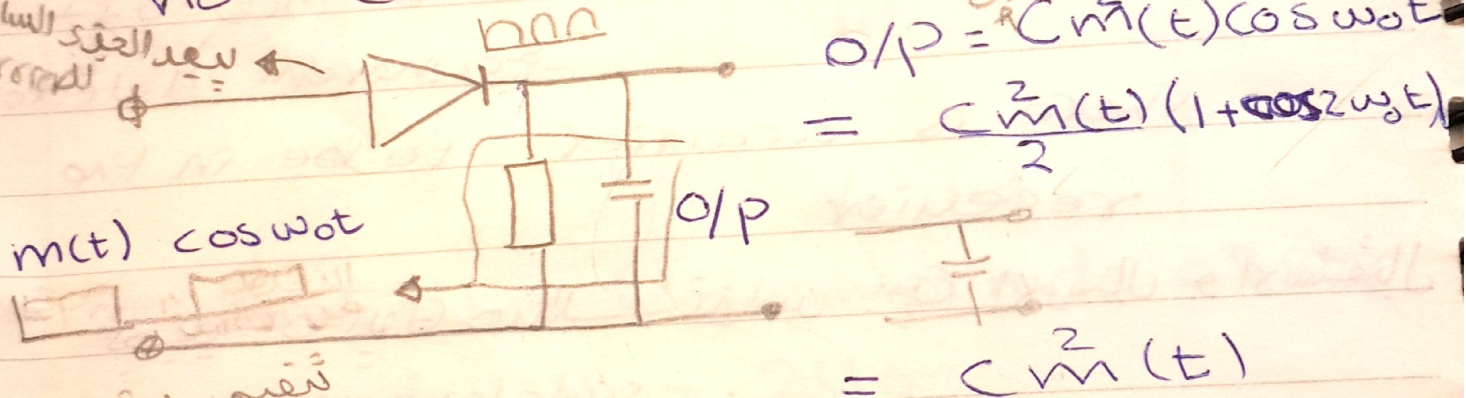


Envelope detector

non coherent detection

non linear device

بعد الجهد السالك
correct

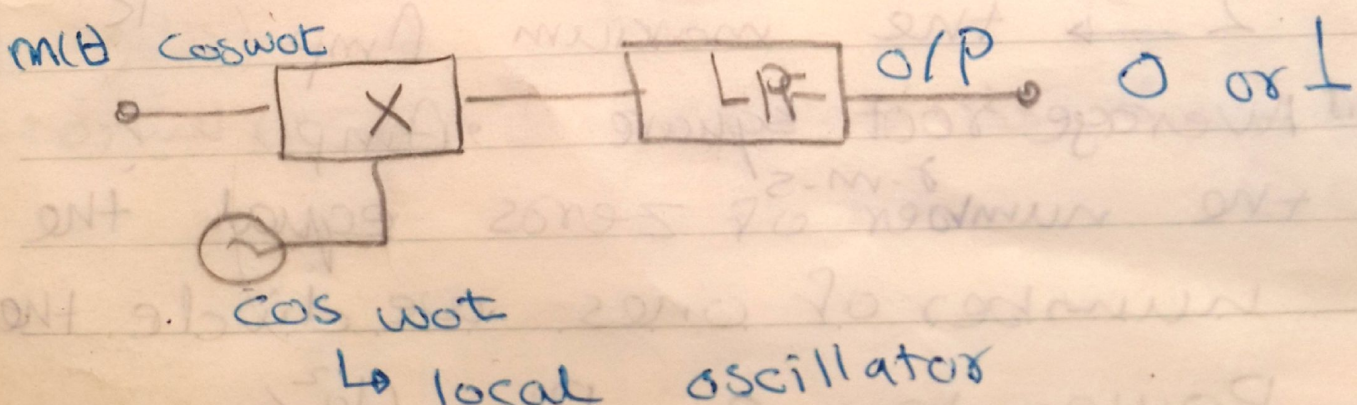


تسمى اوتقود
لأنه
مفرد (مفرد)

$$m(t) = \sqrt{m^2(t)}$$

because it is
= 0, 1

Coherent Detector



$m(t) = 0 \text{ or } 1$
 في $m(t)$ $\cos \omega_c t$ \rightarrow $m(t) \cos \omega_c t$

characteristic of diode

$$C U^2$$

High Freq

LPF $\rightarrow \cos$

$$m^2(t) = m \rightarrow m(t) = 0 \text{ or } 1$$

- coherent detector

there is a carrier to be in the receiver

يجب ان يكون هناك تزامن بين المرسل والمستقبل

Synchronization

discibe the structure operation of ask detectors ? 3 figures

Ask Power

Carrier \rightarrow sinusoidal $\rightarrow P = \frac{U_0^2}{R}$

2 \rightarrow the maximum Amp ($2U_0$)

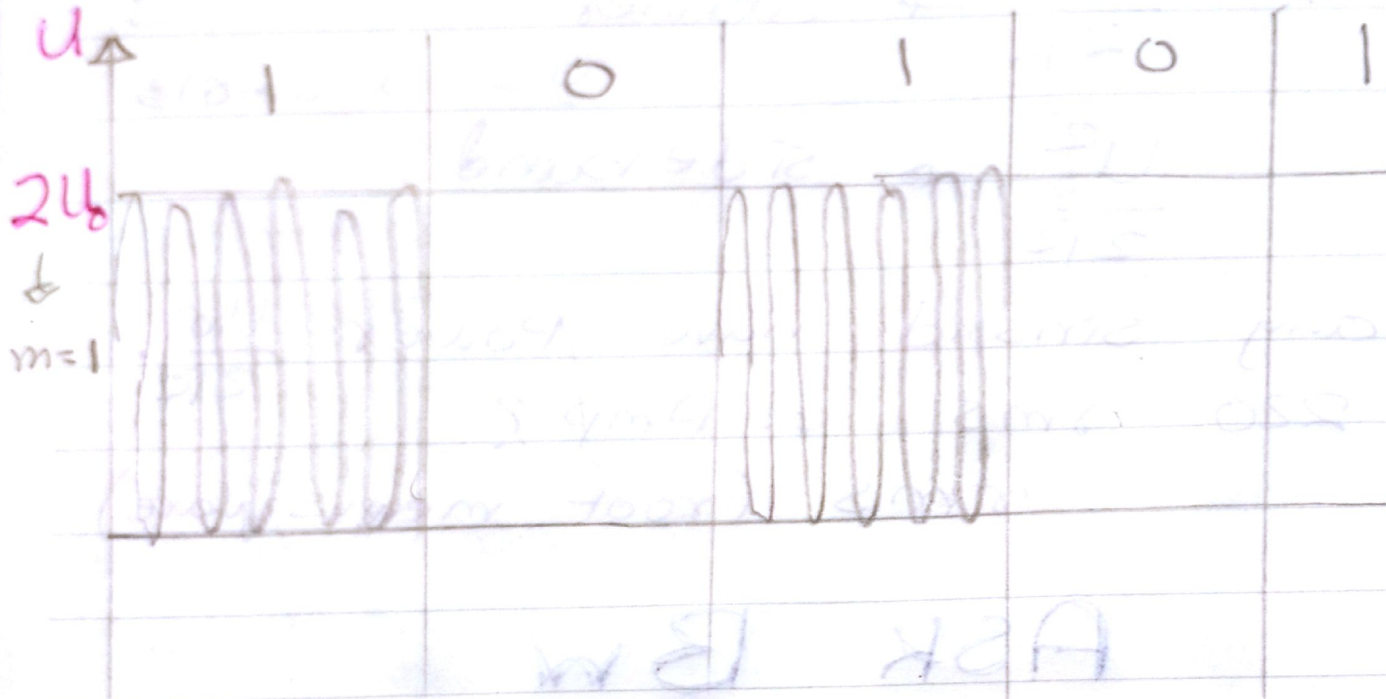
different Average root square Amp ?
 If the number of zeros equal the number of ones we divide the Power by 2 $P_T = \frac{U_0^2}{R}$

$$\left[\frac{U^2}{R} \right] \rightarrow \frac{U_0^2}{R} \rightarrow \text{average Power } \frac{U_0^2}{2R}$$

$$U = U_0(1 \pm m)$$

$$U_{\max} = 2U_0 \quad \text{For } 100\% \text{ mod}$$

$$U_{\min} = \text{Zero}$$



Carrier $U = U_0$ amplitude after

$$\text{mod } U = 2U_0$$

$$P_c = \frac{U_0^2}{2R}$$

$$P_{\text{total}} = \frac{(2U_0)^2}{2R} = \frac{2U_0^2}{R}$$

1) $\frac{P_c}{2}$ is the power of the carrier wave
 2) $\frac{P_c}{2}$ is the power of the sidebands
 3) P_c is the total power of the modulated signal

DC

Carrier Power & Modulation

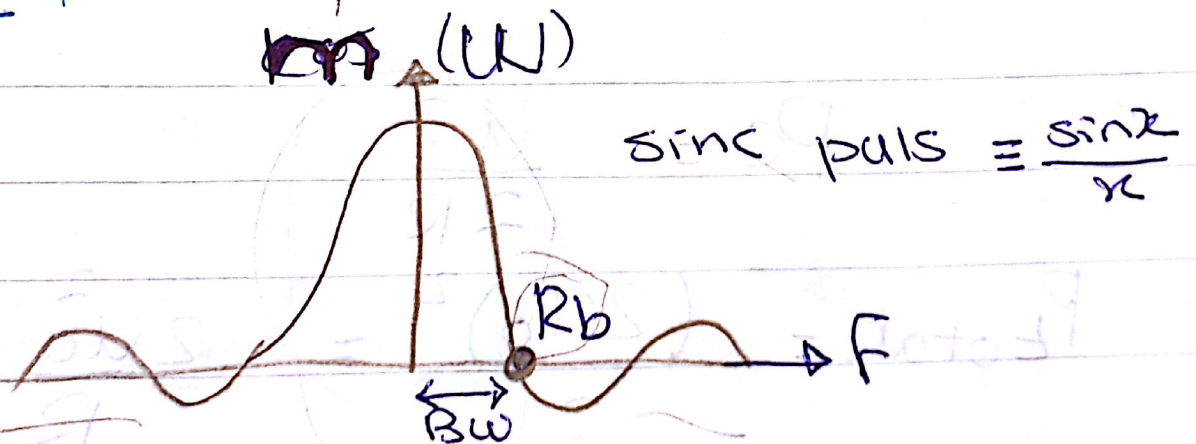
Power of the carrier wave is $\frac{U_0^2}{2R}$ → Carrier

Power of the side band is $\frac{U_0^2}{2R}$ → side band

any sinusoid have Power $\frac{U_0^2}{2R}$.
 rms or Amp?
 rms (root mean square)

* ASK BW *

{ spectrum of the wave
 Fourier Transform (BW) }



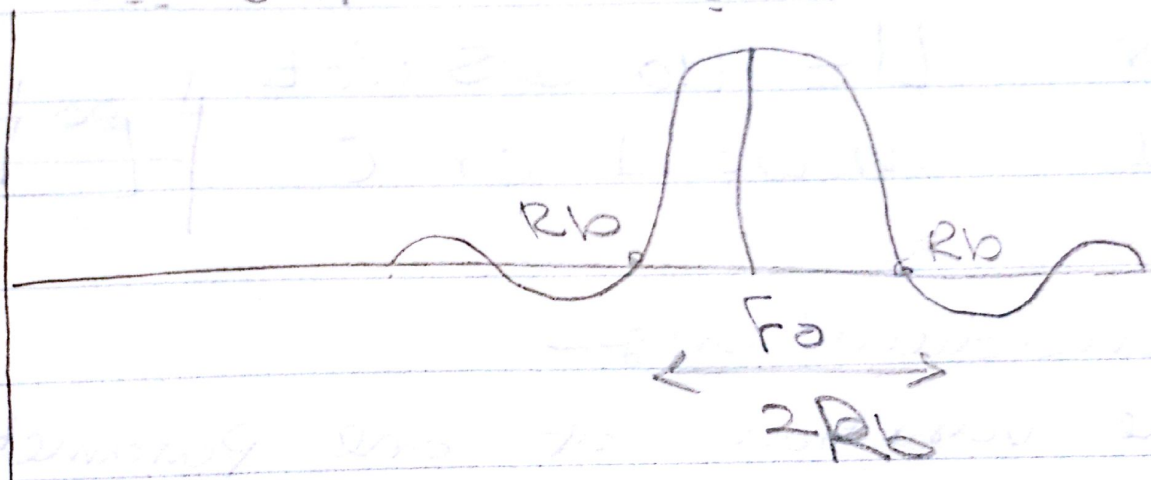
the transform of the digital signal

tails side

m(t) cos wt

modulation of Fourier Transform

cos wt and shift 1st G.T. TF



in AM the band width = $2F_c$

" ASK " " = $2R_b$

5. In ASK, the bandwidth is equal to the baud rate.

1. In ASK, the bandwidth is equal to the baud rate.

lec 6

FSK

sem 7

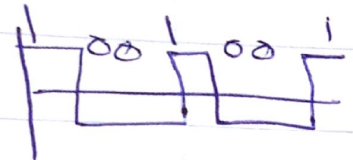
UUI

Frequency shift keying
digital Frequency mod

carrier
signal

$$U = U_0 \cos \omega_0 t$$

$$m(t) = 1 \text{ or } 0$$



modulation

the variation of one parameter of the carrier in according to the signal.

the carrier shift between 2 Frequency depend on 0, 1

Um.

$$1 \rightarrow f_2 (f_{max})$$

$$0 \rightarrow f_1 (f_{min})$$

$$\omega = \omega_0 \pm \Delta\omega$$

Frequency deviation



$$F = F_0 \pm \Delta F$$

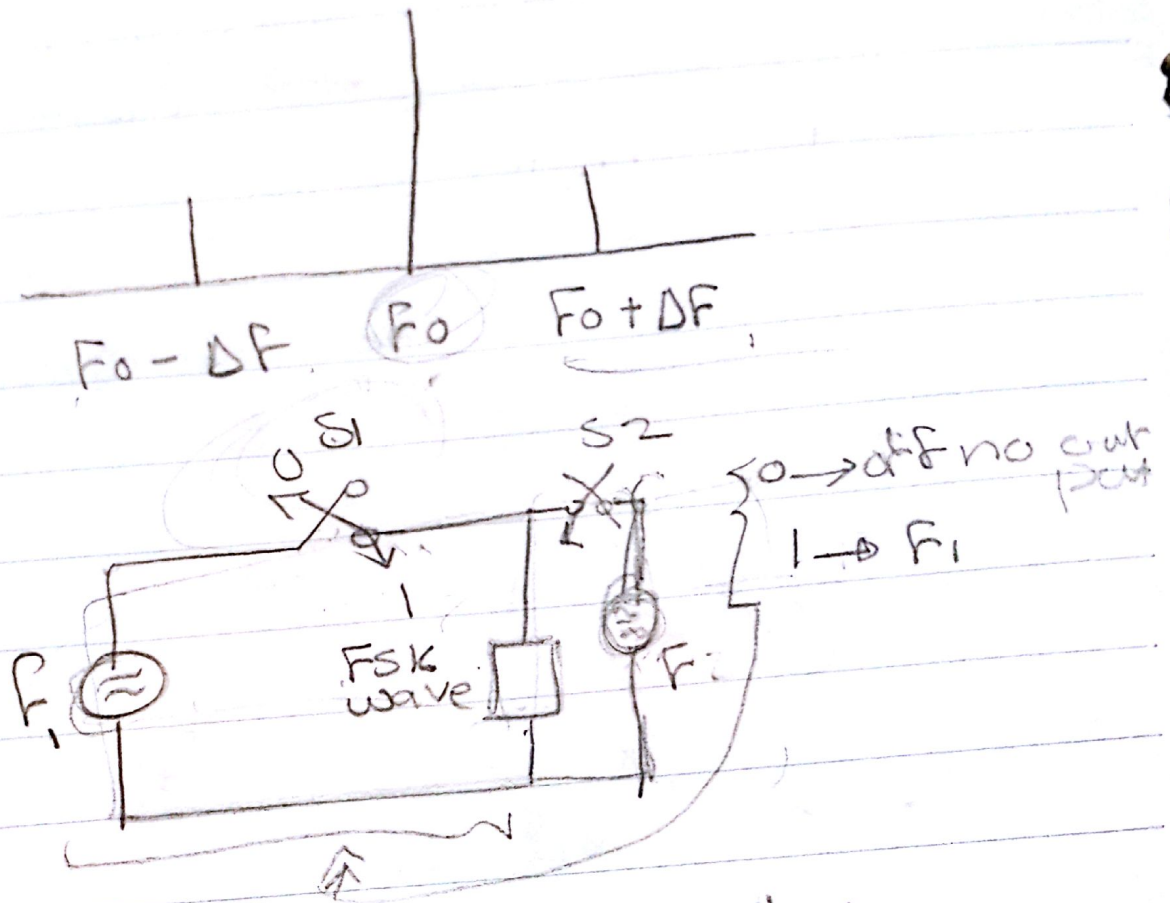
ω_0

IF g-

$$m(t) = 1 \rightarrow \omega_0 + \Delta\omega$$

$$m(t) = 0 \rightarrow \omega_0 - \Delta\omega$$

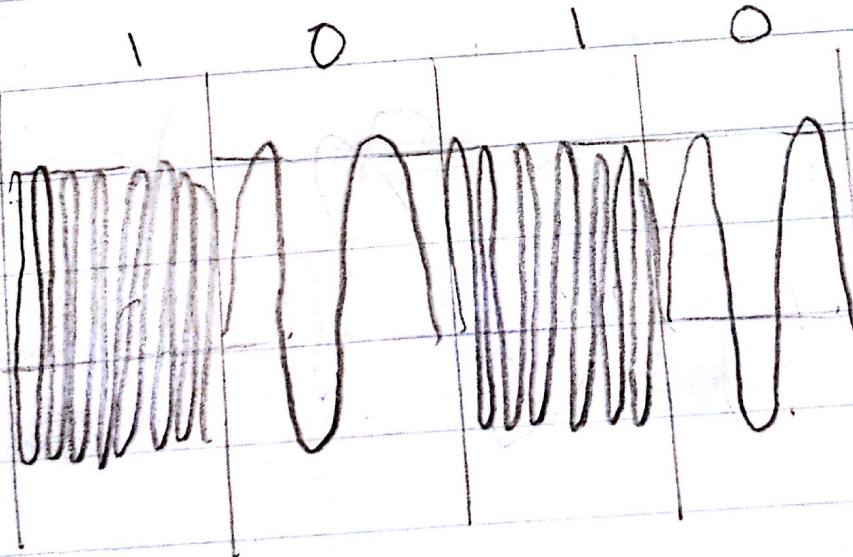
$$f_0 - \Delta F$$



S_1, S_2 = change over switch

$S_1 \rightarrow \text{ON}$, $S_2 \rightarrow \text{OFF}$

over



$F_1 \rightarrow$ lower Freq

$F_2 \rightarrow$ high Freq

FSK \rightarrow 2,000 K

obtain the Band width
of a pulse with amplitude
1V and width $\tau = 25\mu s$

$$Bw = \frac{2\pi}{\tau}$$

$$Bw = \frac{2\pi}{25\mu s} = \underline{\hspace{2cm}} \text{ Hz}$$

NO.

Lec 6

DATE

Tests-

Q1:- A TV signal with bandwidth $0 - 4.5 \text{ MHz}$ is sampled using sampling rate 20% higher than the nyquist (sampling principle rate) and the samples are quantized to 1024 level. If the signal is transmitted using ASK find the minimum BW required to carry the signal?

Q2:- with diagrams and equations discuss in detail the generation and detection of ASK

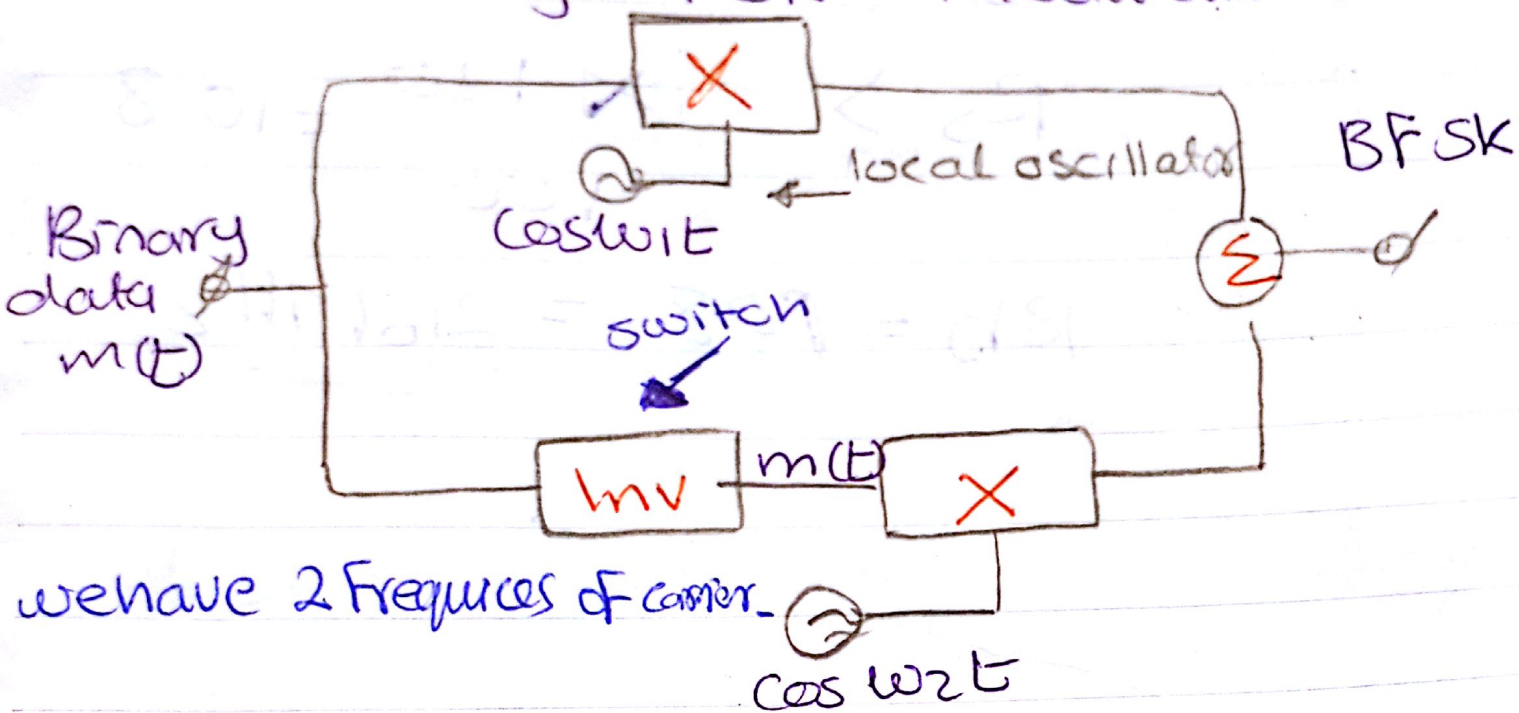
$$R_s > 2F\omega$$

$$R_s > \frac{91 \times 120}{100} \approx 10^{-8}$$

$$R_b = 108 = \underline{\underline{216 \text{ MHz}}}$$

BFsk used in cable & radio communication system.

Binary FSK modulator



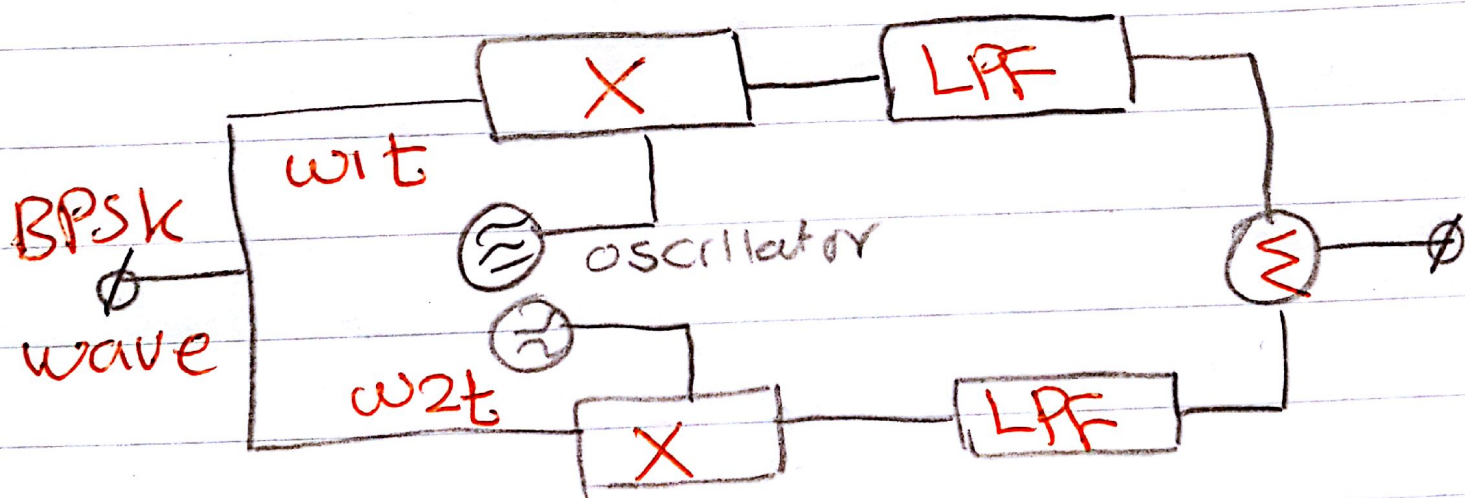
we have 2 Frequencies of carrier.

$$m(t) = 0 \text{ OR } 1.$$

$$m(t) = 1 \longrightarrow \cos wt$$

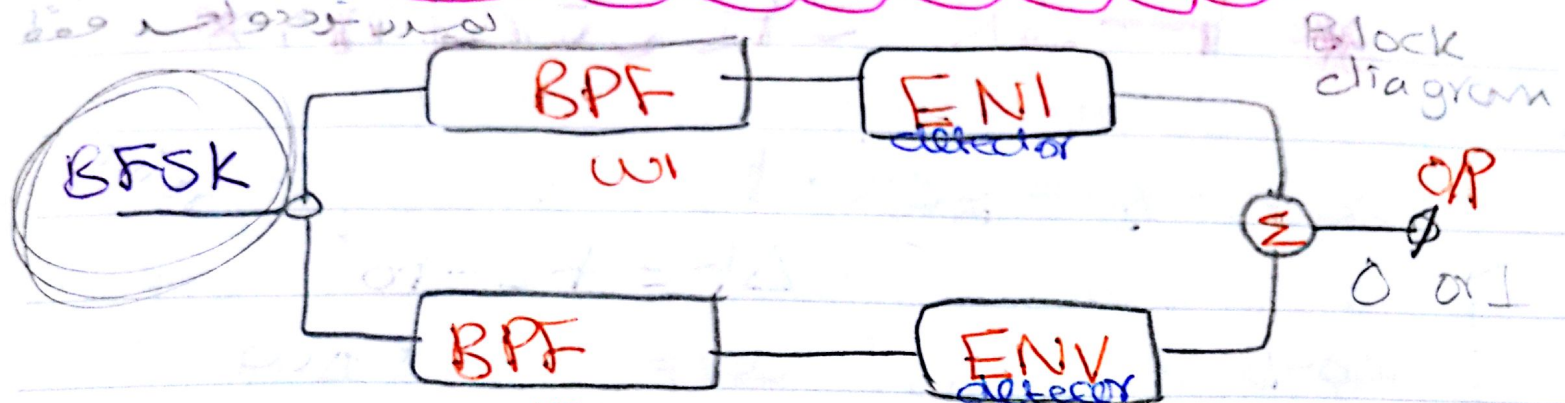
$$m(t) = 0 \longrightarrow \cos wt$$

Coherent Detector



خبرداران با کوی و کمال و سنجش و سنجش و سنجش

* Envelope detector *



Like ^{ω2} Amplitude detection

BFSK modulators

$m(t) = 1 \rightarrow \cos \omega_1 t$, $m(t) \rightarrow \text{zero}$

$m(t) = 0 \rightarrow$

ω_2 after mod give puls

ENV detector + LPF + ω_2

* FSK Spectrum *

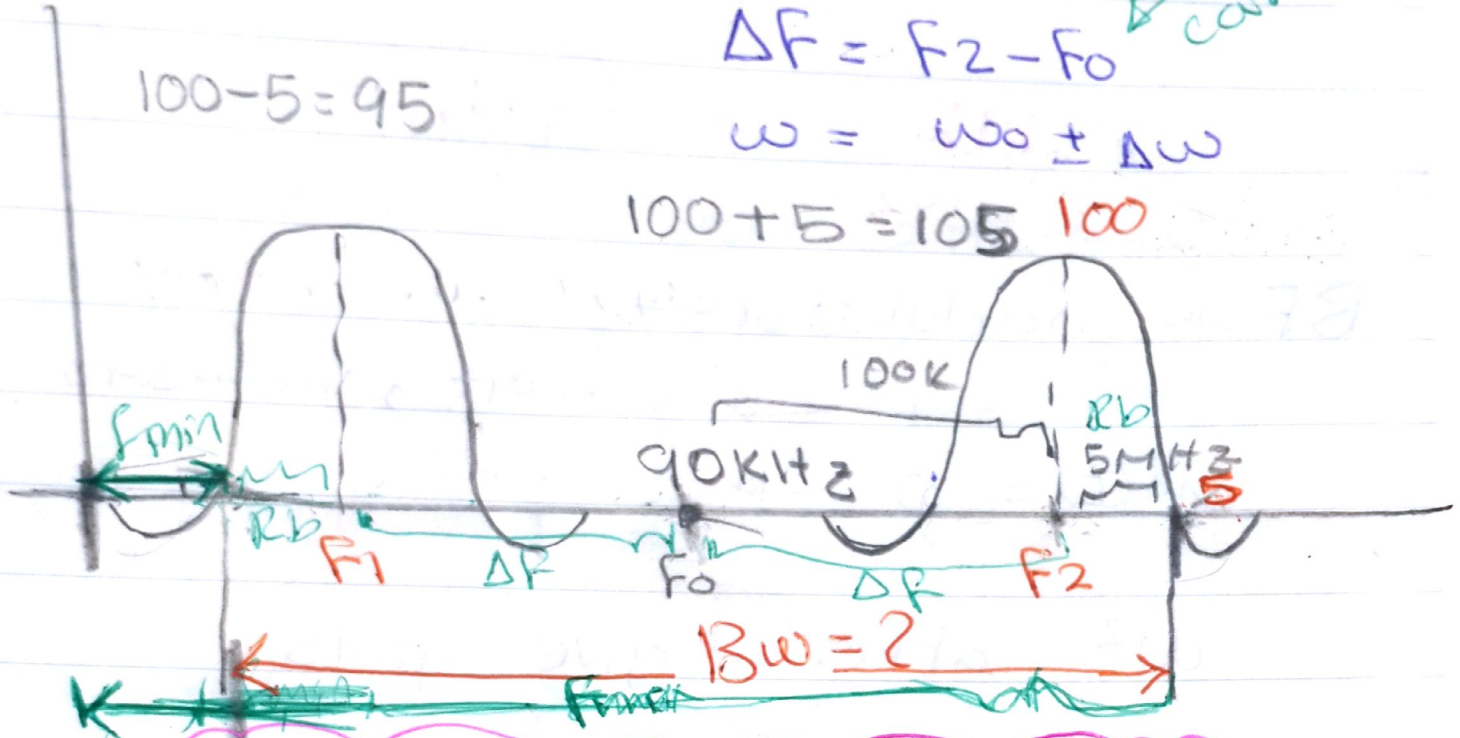
Spectrum of Am x 2

$$FSK = 00K$$

$$\Delta F = F_2 - F_0$$

$$\omega = \omega_0 \pm \Delta\omega$$

$$100 + 5 = 105 \quad 100$$



$$Bw = 2Rb + 2\Delta F$$

Find the Bandwidth of Fm radio alrabiaa?

Carson's Rules

$$Bw = 2F_{\Omega} + 2\Delta F$$

$$\Delta F = 75, F_{\Omega} = 15kHz$$

$$Bw = 15 \times 2 + 75 \times 2 = 180 kHz$$

$$\Delta F = \frac{f_{max} - f_{min}}{2}$$

* Example 8 -

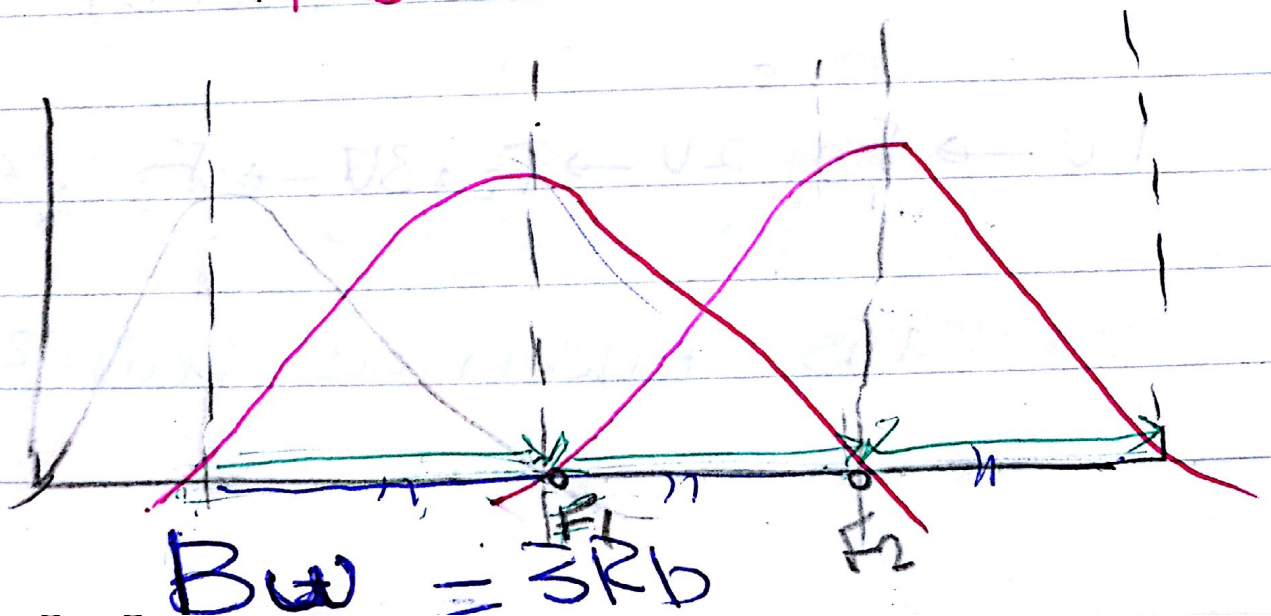
If the carrier of aFSK transmission
 90 kHz , $R_b = 5 \text{ Mbps}$
 the maximum deviation 100 kHz

Find the Following:-

- 1- The maximum Frequency at the output = $90 + 5 = 95 \text{ MHz}$
 - 2- The minimum = $90 - 5 = 85 \text{ MHz}$
 - 3- The BW required
- sol:-

Orthogonal BFSK:-
 Spectrum of Orth BFSK

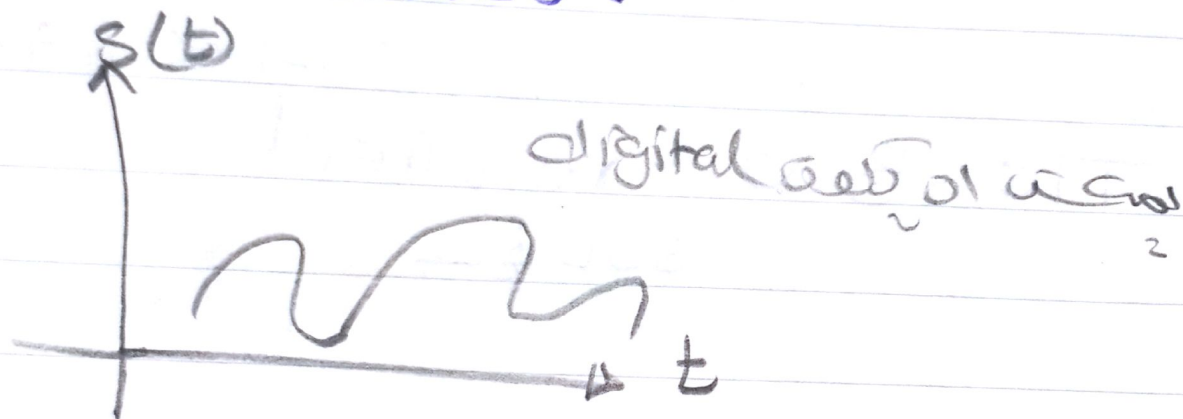
$$R_b = F_2 - F_1$$



4-ary FSK

4 FSK

It means that digital signal is not 0, 1
It may have two and more levels.



we have 4 Frequencies in the output F_1, F_2, F_3, F_4

Frequency carrier

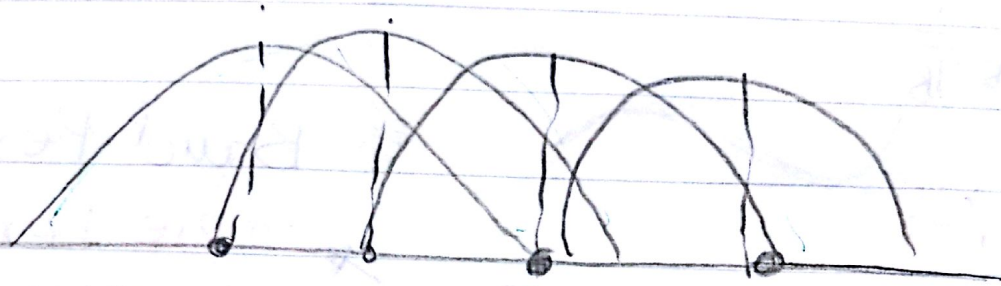
here we need 4 levels (volts)

$1V \rightarrow F_1, 2V \rightarrow F_2, 3V \rightarrow F_3, 4V \rightarrow F_4$

00 → serial to parallel converter
01
10

Data is taken as pair (2 bits)

Spectrum

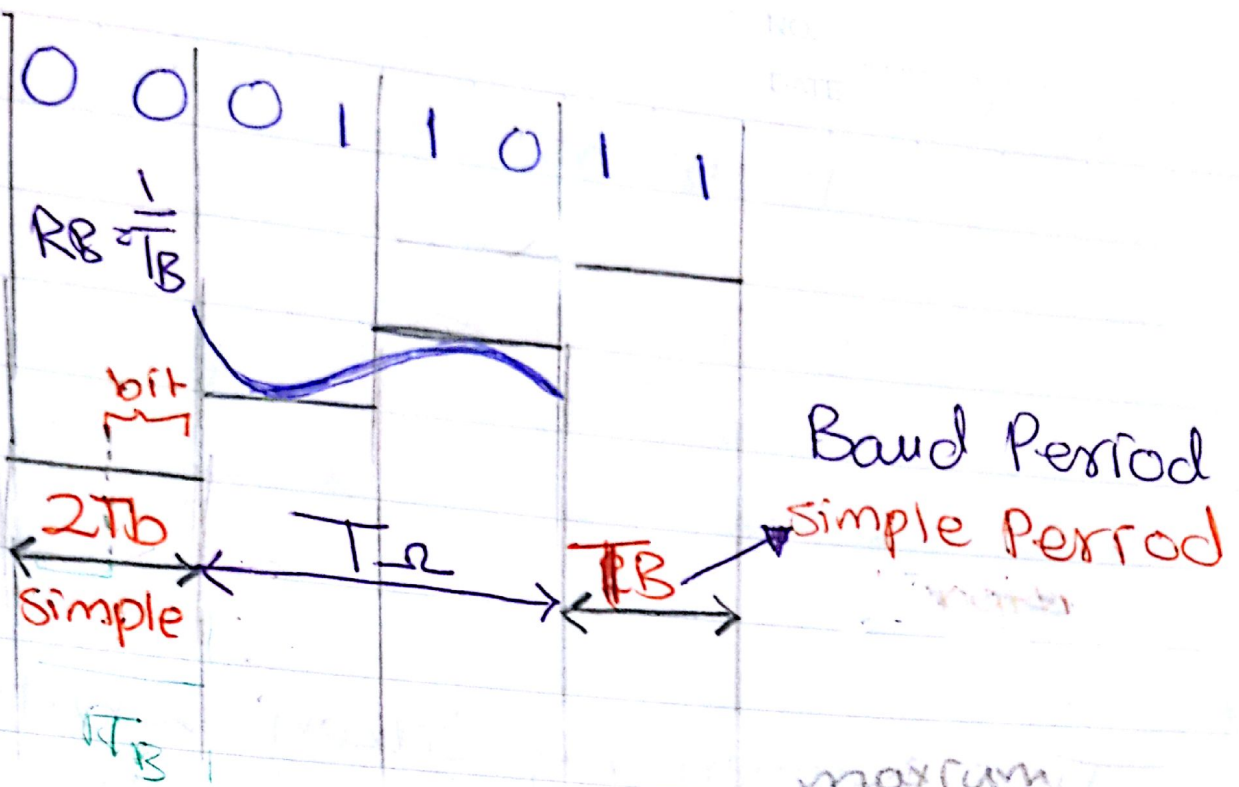


This happen by taken 2 bits
at the same time to
represent a level. and this
can be done by using **serial to Parallel
converter**

2 bits enter to modulator as
the same time and this
two bit have one level.

00	→	Level → 0V
01	→	Level → 1V
10	→	Level → 2V
11	→	Level → 3V

bits 2 delay 3
2 bits 2 mod



$$T_r = 3400 \text{ } \mu\text{s}$$

السرعة البت Period ال Period ال
السرعة البت تقسيم ال
السرعة البت

The rate of modulation, simple is called Baud, simple rate.

$$* R_B = R_b / 2$$

$$* T_B = 2T_b$$

$$\frac{1}{T_B} = \frac{1}{2T_b}$$

$$R_B = \frac{R_b}{2}$$

$$* 2T_b = T_B$$

$$* R_b = 2R_B$$

$$* T_r = 4T_b = 2T_B$$

analogy

NO.

DATE

$$F_{\Omega} = \frac{1}{T_{\Omega}}$$

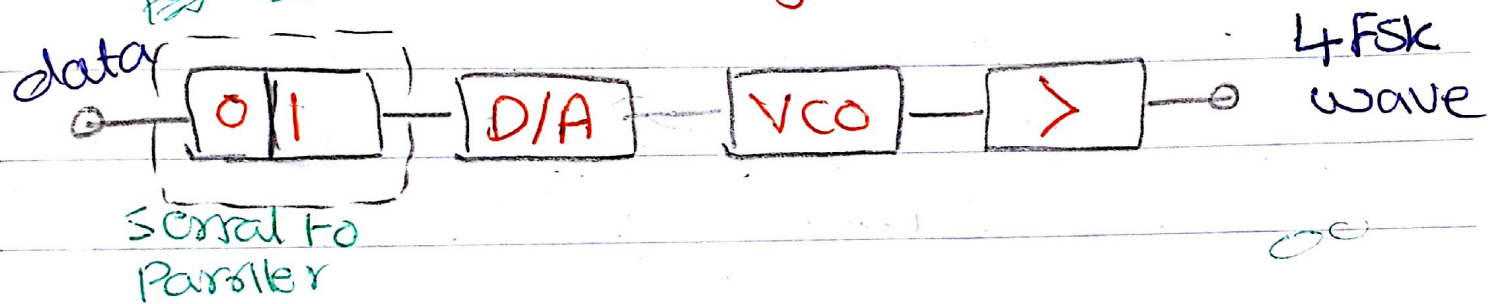
$$F_{\Omega} = \frac{R_b}{4} = \frac{R_B}{3}$$

الشارة دَمَد قِي 2level دَقَقَا لَطَار عَنَد
 اَن اَل level دَمَد قِي bit

Describe the structure and operation of a 4FSK modulator and detection with diagrams show the spectrum diagram and find the Bandwidth of the input data bit Rate

$R_b = 9 \text{ mbps}$

block diagram 4FSK



Serial to Parallel

Bit rate = $2 \times$ Baud rate
 $2 \times$ sample rate

D/A \rightarrow converts level of voltage each volt
(2/4 level conv) have

VCO \rightarrow voltage control

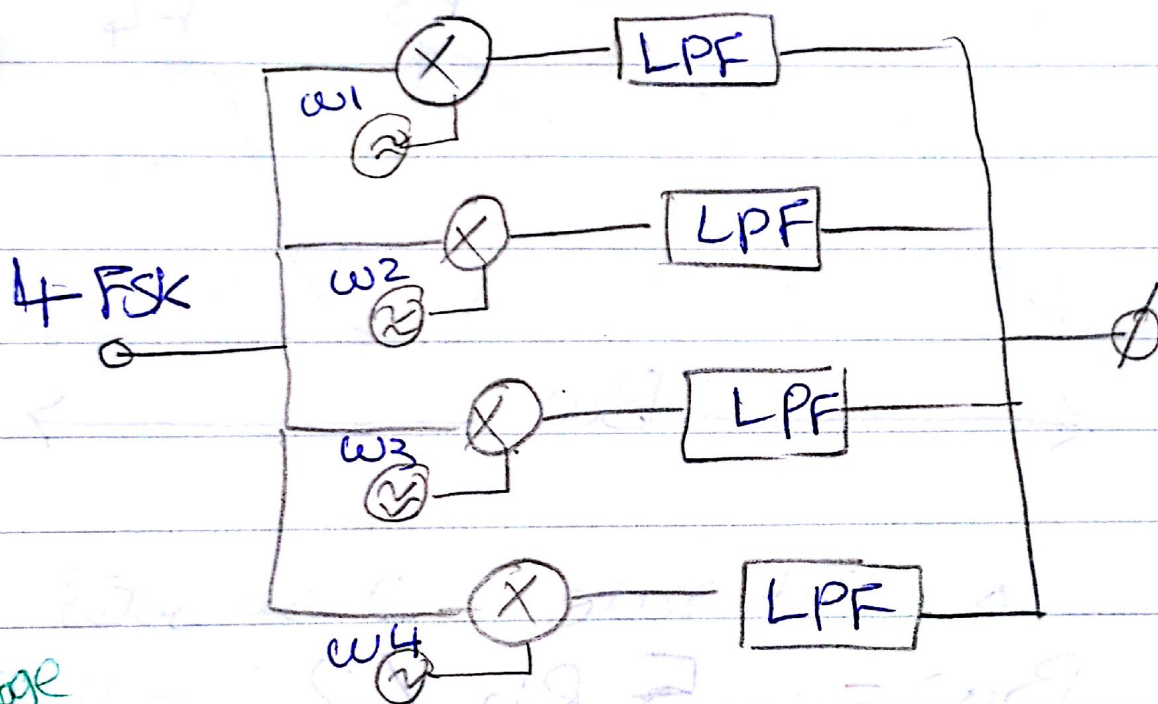
oscillator (Fm)

Baud = number of samples per rate second

Bit rate & number of samples per second

> 8 amplifier

block diagram of detector of 4 FSK



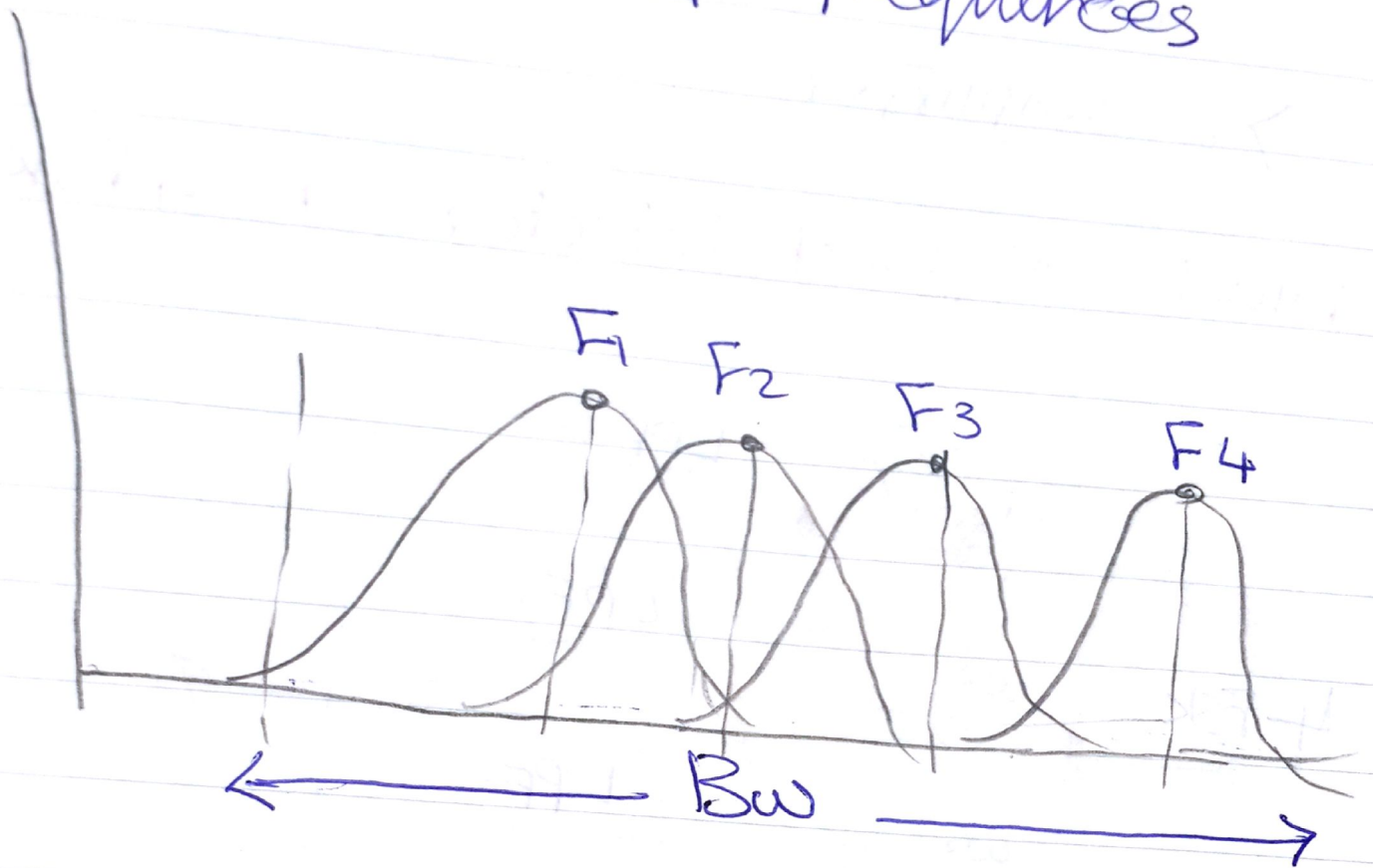
logic only one channel is operated because :-

(1) $\cos \omega_1 t * \cos \omega_1 t = \frac{1}{2} (1 + \cos 2\omega_1 t)$

we have Dc \rightarrow plus $\frac{1}{2} \cos 2\omega_1 t$

$\cos \omega_1 t * \cos \omega_2 t = 2$ high frequency
the low Pass filter don't Passes

Orthogonal 4 FSK
There are 4 Frequencies



$Bw \rightarrow$ bit rate Δ is output

$$Bw = 5 R_b / 2 = 5RB$$

2 bit / 2 bit

Example

If the lowest Frequency = 95 MHz
& $R_b = 10 \text{ Mbps}$ find the
highest Frequency to be reached
after modulation -

$$B_w = 5 \times 10 \text{ Mbps} / 2$$

Digital Phase Modulation

The phase of the carrier will vary with digital signal (0, 1)

• the output have 2 phases

* one phase depends on 0

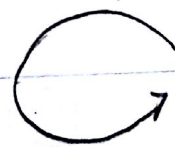
* second phase depends on 1

let us describe the phase with a vector diagram or phase diagram.

The phase range from 0 to 360°

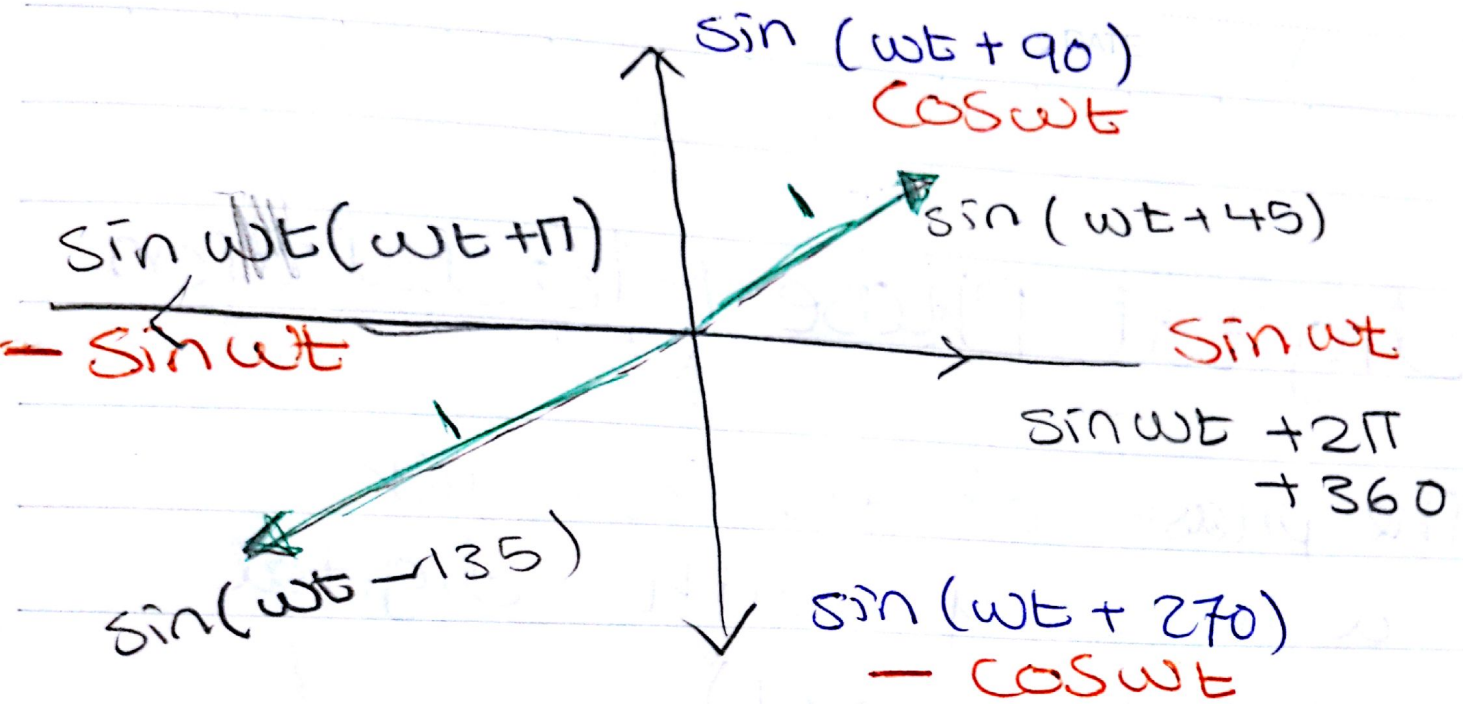
The vector starts with x axis and rotate counter clockwise.

magnitude = 1



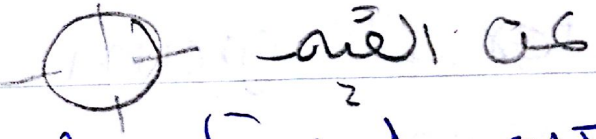
Let this x axis represents the carrier frequency $\sin \omega t$ → the phase when we started

Zero إلى 360



we mean that the phase of $\sin \omega t$ will change with the digital signal.

لما نكتب الفولتية في دائرة



في كل لحظة من الزمن
2 phases or 2 vector

$$-\sin \omega t - 90^\circ = \cos \omega t$$

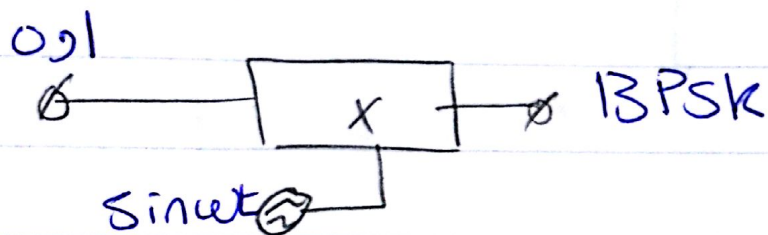
* BW \rightarrow Band rate

BPSK

Binary Phase Shift Keying

block diagram

digital input



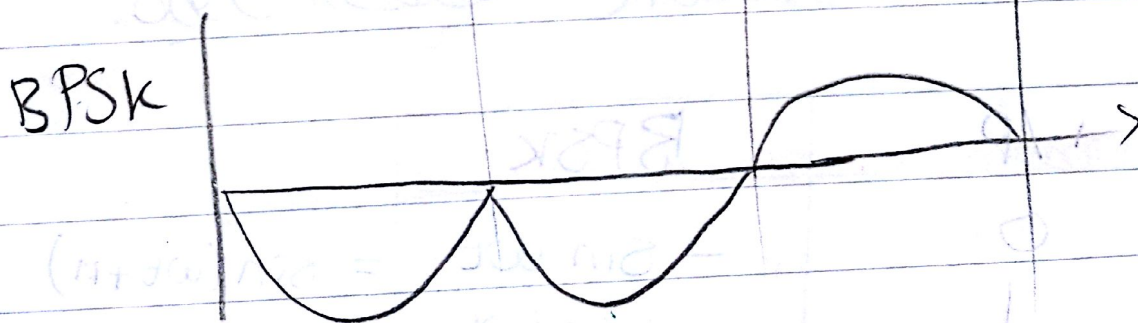
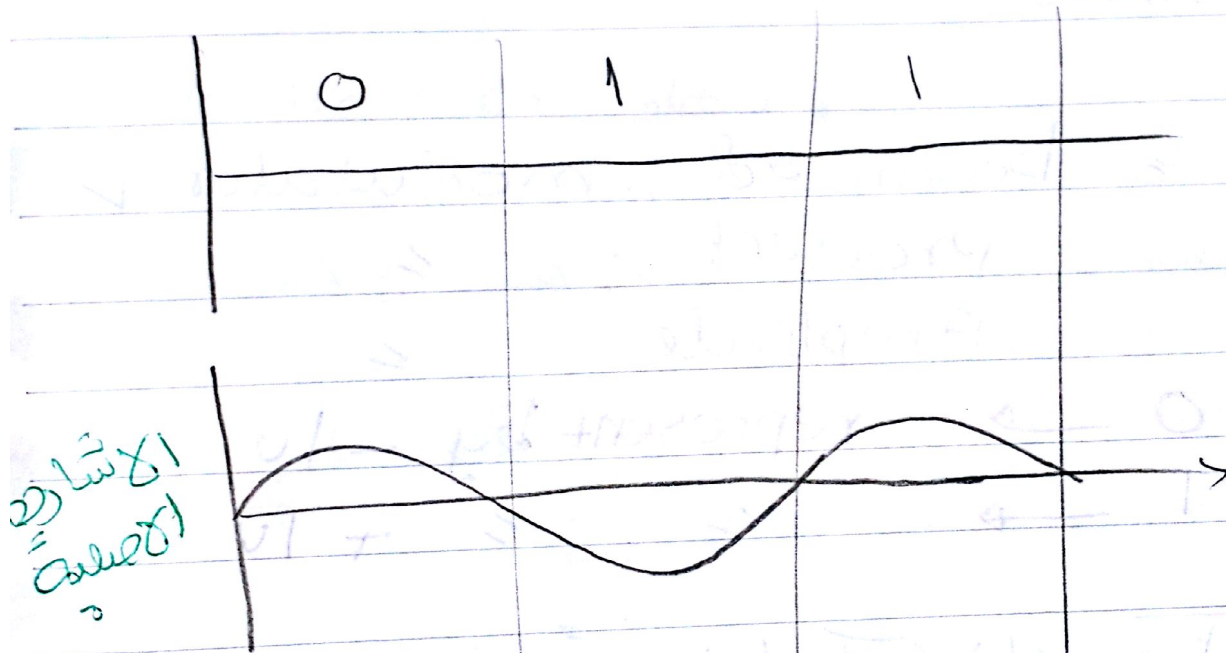
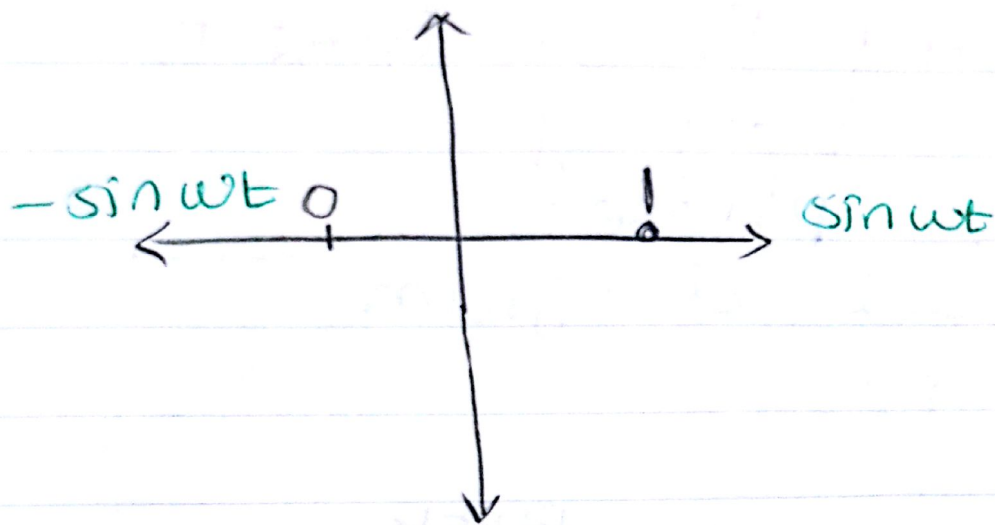
x is a balanced modulator ✓
 product \leq
 Amplitude \leq

out of phase 0 \rightarrow represent by $-1V$
 in phase 1 \rightarrow " $+1V$

Truth Table

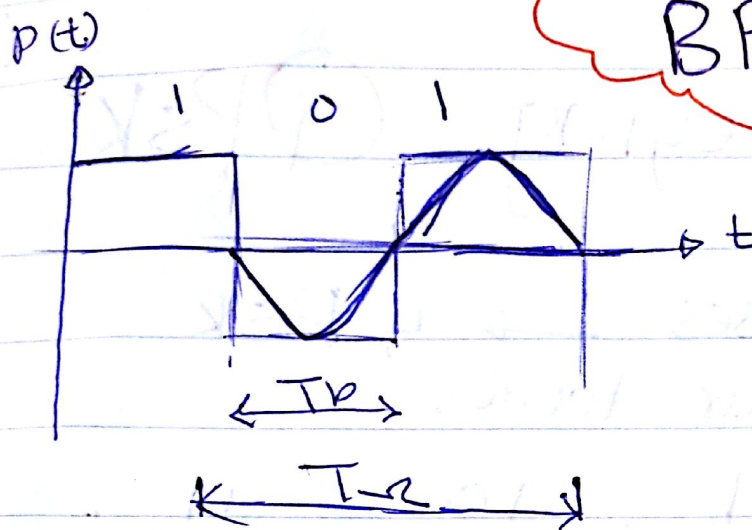
I/P	BPSK
0 ✓	$-\sin \omega t$
1 ✓	$\sin \omega t$

$= \sin(\omega t + \pi)$



1 — 0 no Toggle Does X
0 — 0 Toggle

Bandwidth BPSK



$$R_b = \frac{1}{T_b} = \frac{1}{T_B} = R_B$$

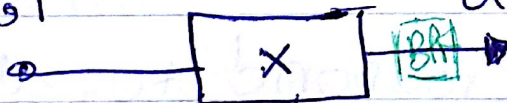
$$T_2 = \frac{1}{f_2} = 2T_b$$

$$f_2 = R_b/2 = R_B/2$$

* BPSK modulator:-

Carrier \rightarrow signal \rightarrow

0, 1

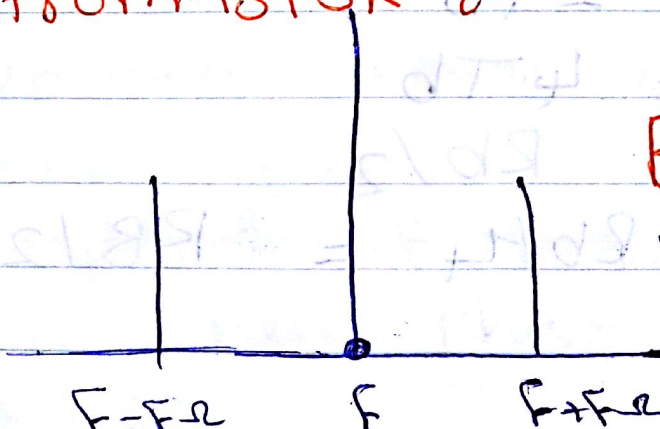


$$u(t) = \sin 2\pi f_c t \cdot \sin 2\pi f_m t$$

$$\sin \omega_c t$$

$$u(t) = \frac{\cos 2\pi (f - f_c) t}{2} \frac{\cos 2\pi (f + f_c) t}{2}$$

Spectrum BPSK :-

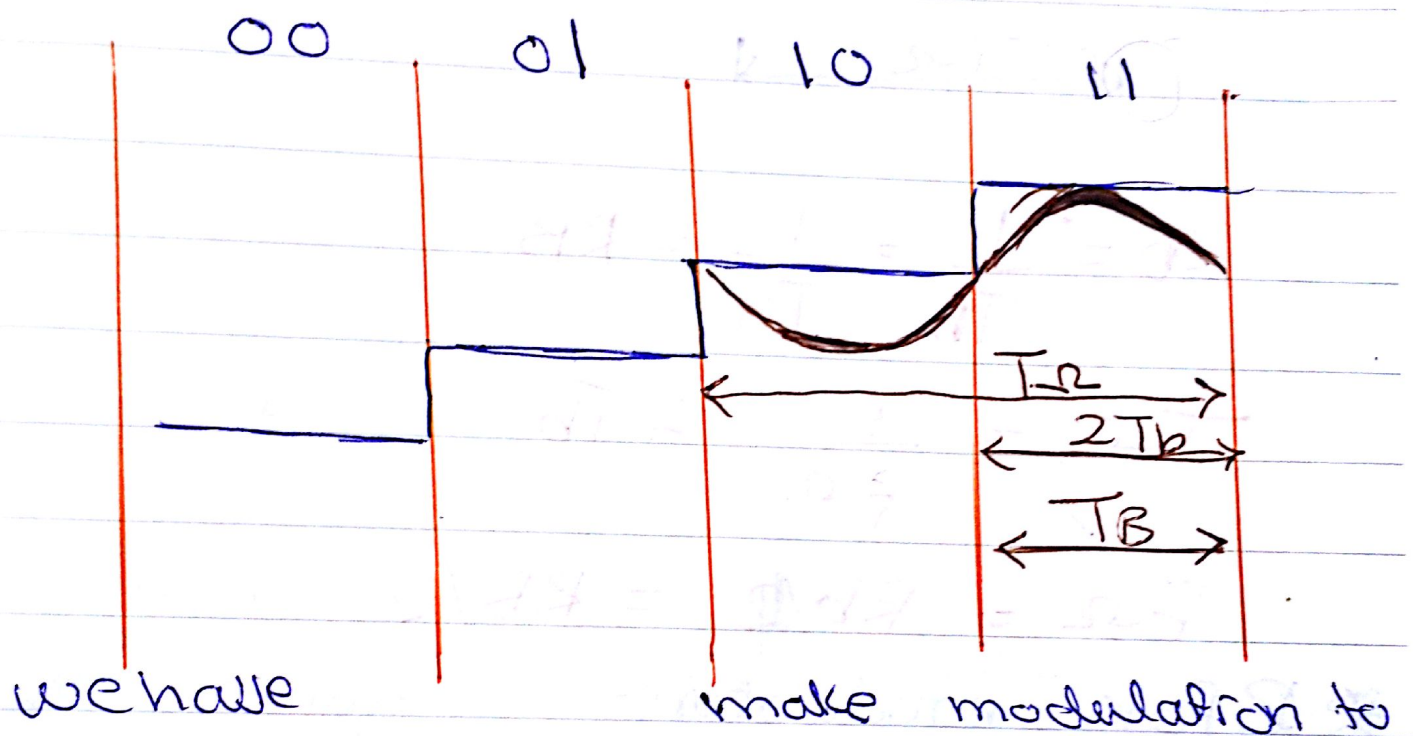


$$B_w = 2f_2 = R_b = R_B$$

Bw From Baud rate and not from the bit rate.

Quadrature Phase Shift Keying QPSK

we have 4 Phases \rightarrow 4 PSK
we need Four levels



the carrier.

each 2 bits correspond to a level
each level will give a phase

$$T_B = 2T_b$$

$$T_Q = 4T_b$$

$$R_B = R_b / 2$$

$$F_Q = R_b / 4 = R_B / 2$$

QPSK

NO.

DATE

- 1- Block diagram
- 2- Truth Table
- 3- Phase diagram
- 4- Detector
- 5- spectrum & Bw
- 6- Example

M-ary
bits
numbers

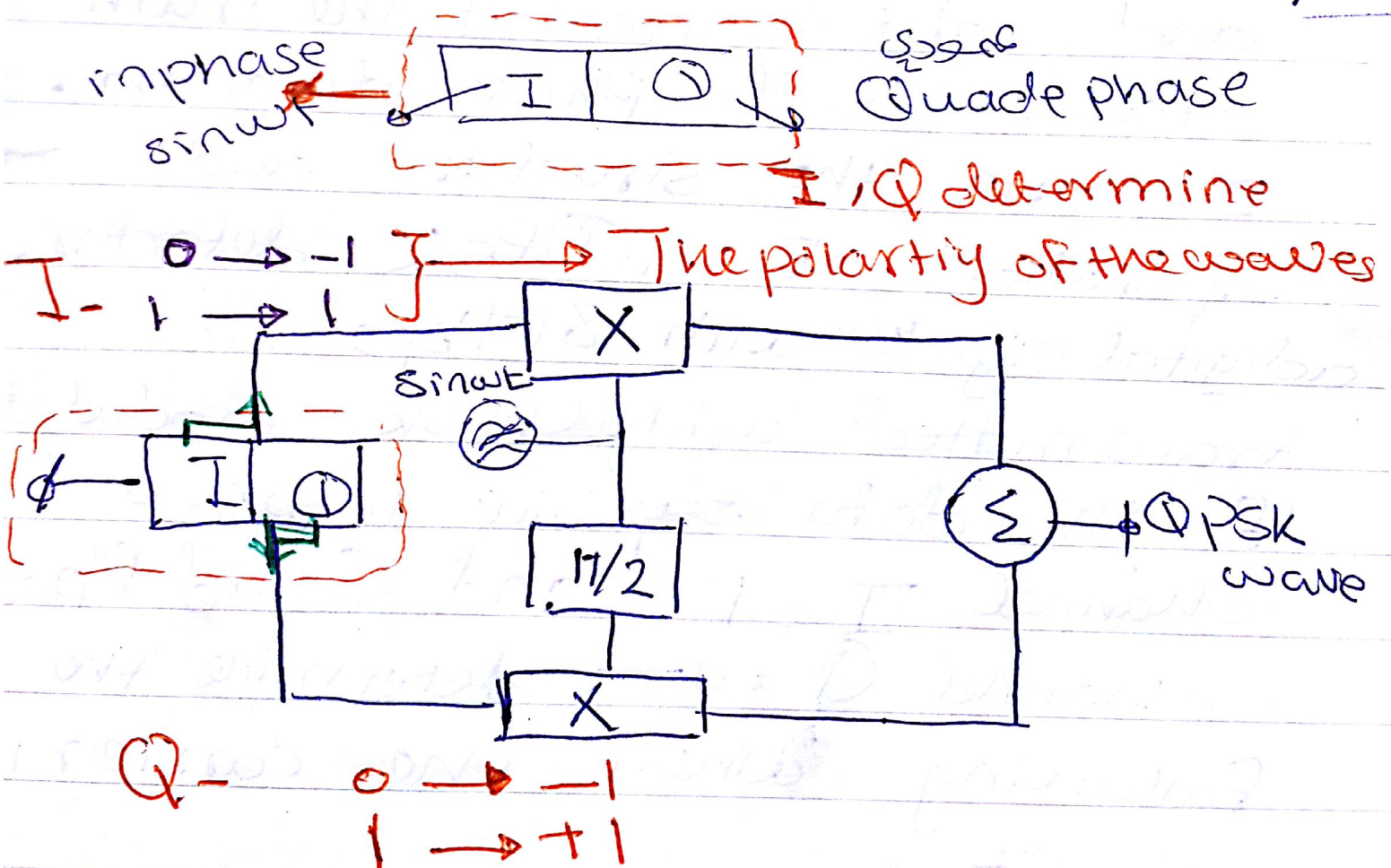
The subject As a question & draw a block diagram of a QPSK modulator and obtain from it the truth table and phase diagram.

Explain the structure and operation of a QPSK detector. A digital signal with 8 Mbps is transmitted using QPSK modulator. If the data sequence in phase channel $I = 1$ and Quad Phase channel $Q = 0$ - determine the following when the carrier is $\sin \omega t$.

- 1- Amplitude of the modulated wave
- 2- The new phase of the modulated wave

- 3- The BW of the modulated wave
- 4- The highest frequency to reached after modulation.
- 5- sketch the wave form of the output directly below the carrier wave.

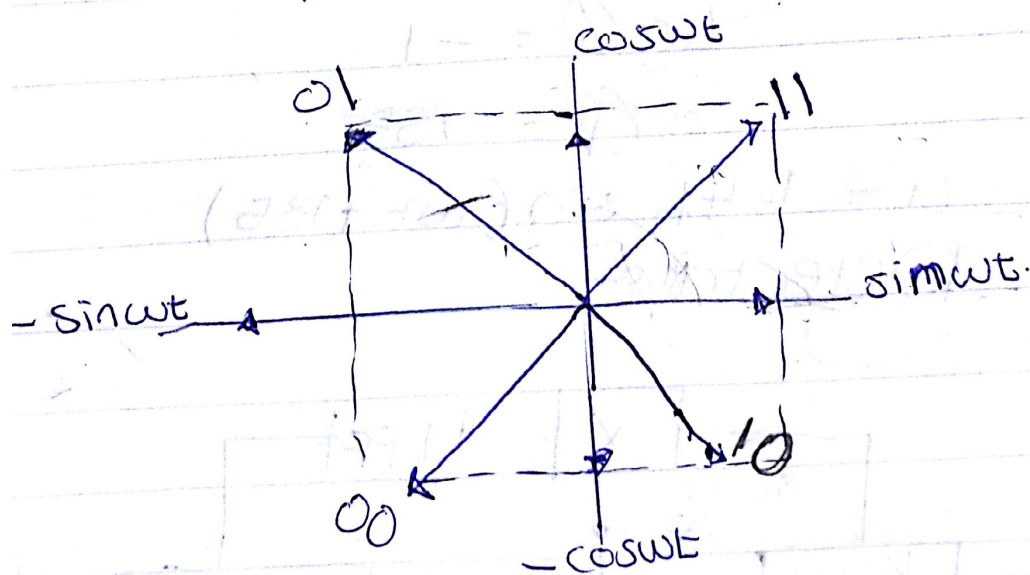
(we want 4 levels \rightarrow is done by taken 2 bits instead of one by a serial to parallel converter)



with 4 Phase

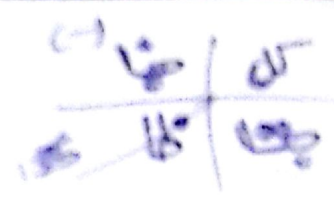
I	Q	DPSK wave	
0	0	$-\sin \omega t - \cos \omega t$	135°
0	1	$-\sin \omega t + \cos \omega t$	135°
1	0	$\sin \omega t - \cos \omega t$	45°
1	1	$\sin \omega t + \cos \omega t$	45°

The angle was $\sin \omega t$
phase diagram



$11 \rightarrow \sin(\omega t + 45^\circ)$
 $01 \rightarrow \cos(\omega t + 135^\circ)$
 $10 \rightarrow 1.41 \sin(\omega t + 45^\circ)$
 $00 \rightarrow 1.41 \cos(\omega t + 135^\circ)$

360/4 = 90°



$\frac{360}{4} = 90^\circ$ phase of DPSK

Find the wave of DPSK if $I=0$
 $Q=1$

$u = -\sin \omega t + \cos \omega t = A \sin(\omega t + \phi)$

$u = A \cos \phi \sin \omega t + A \sin \phi \cos \omega t$

$A \cos \phi = -1$

$A \sin \phi = 1$

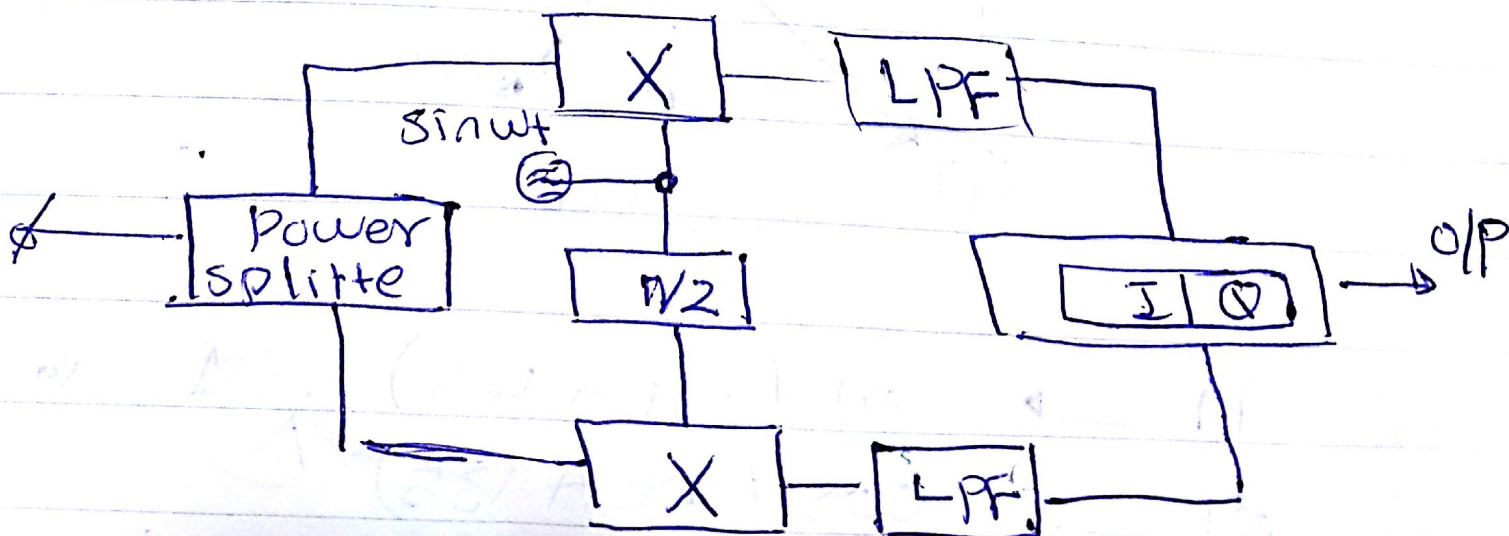
$A = 1.41$

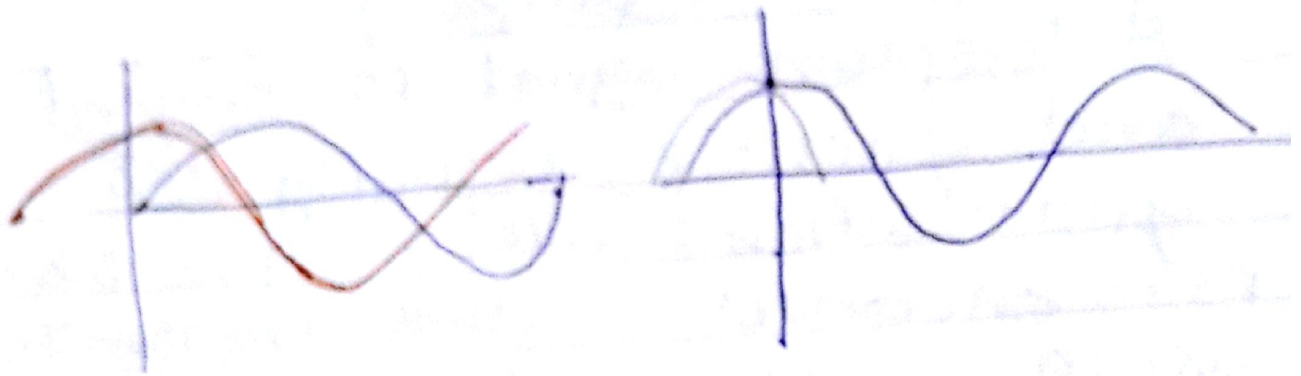
$\tan \phi = -1$

$\phi = 135^\circ$

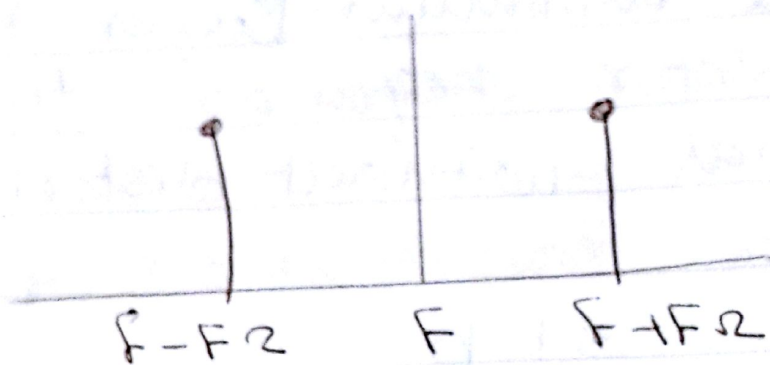
$u = 1.41 \sin(\omega t + 135^\circ)$

Detector



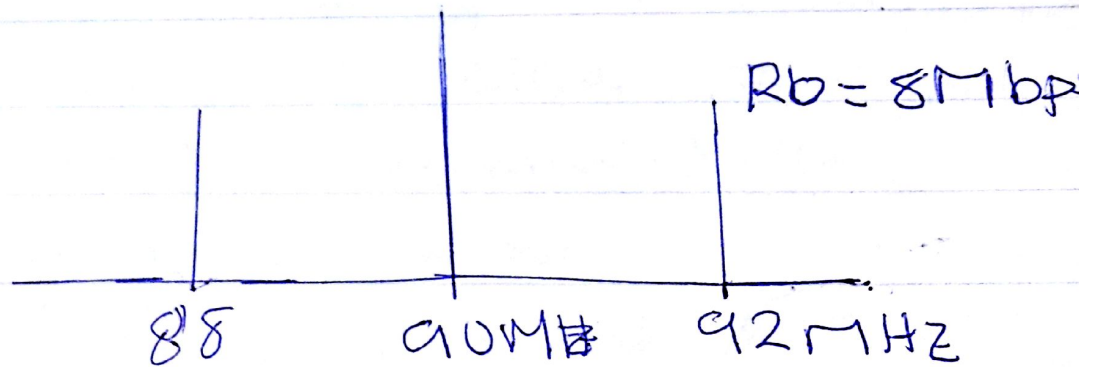


spectrum



$$f_r = R_b/4$$

$$B_w = \underline{R_b/2} = R_B$$



$$f_r = 92 \text{ MHz}$$

$$f_r = R_b/4 = \frac{8}{4} \text{ MHz} \quad B_w = 92 - 88 = 4$$

$$B_w = 2f_r = 2 \times 2 = 4$$

Quiz

A telephone signal is sampled and quantized using 128 quantization level. The signal is encoded and transmitted using orthogonal BPSK modulation. Find the minimum Bw of the modulator required to transmit the signal without distortion.

$$Bw = 2R_b + 2\Delta f$$

$$Bw = 3R_b$$

$$L = 128 \quad n = 7 \quad R_s = 8 \text{ KHz}$$

$$R_b = R_s \times n = 8000 \times 7$$

$$\begin{array}{r} 8000 \\ \times 7 \\ \hline 56000 \end{array}$$

المشكال بإضافة الفهرست التالي

8Phase Shift Keying

8PSK

- what is relation between sample rate and bit rate in 8PSK?
- Draw a block diagram of an 8PSK and explain in detail its operation
- obtain from this diagram the truth table Phase diagram, space diagram.

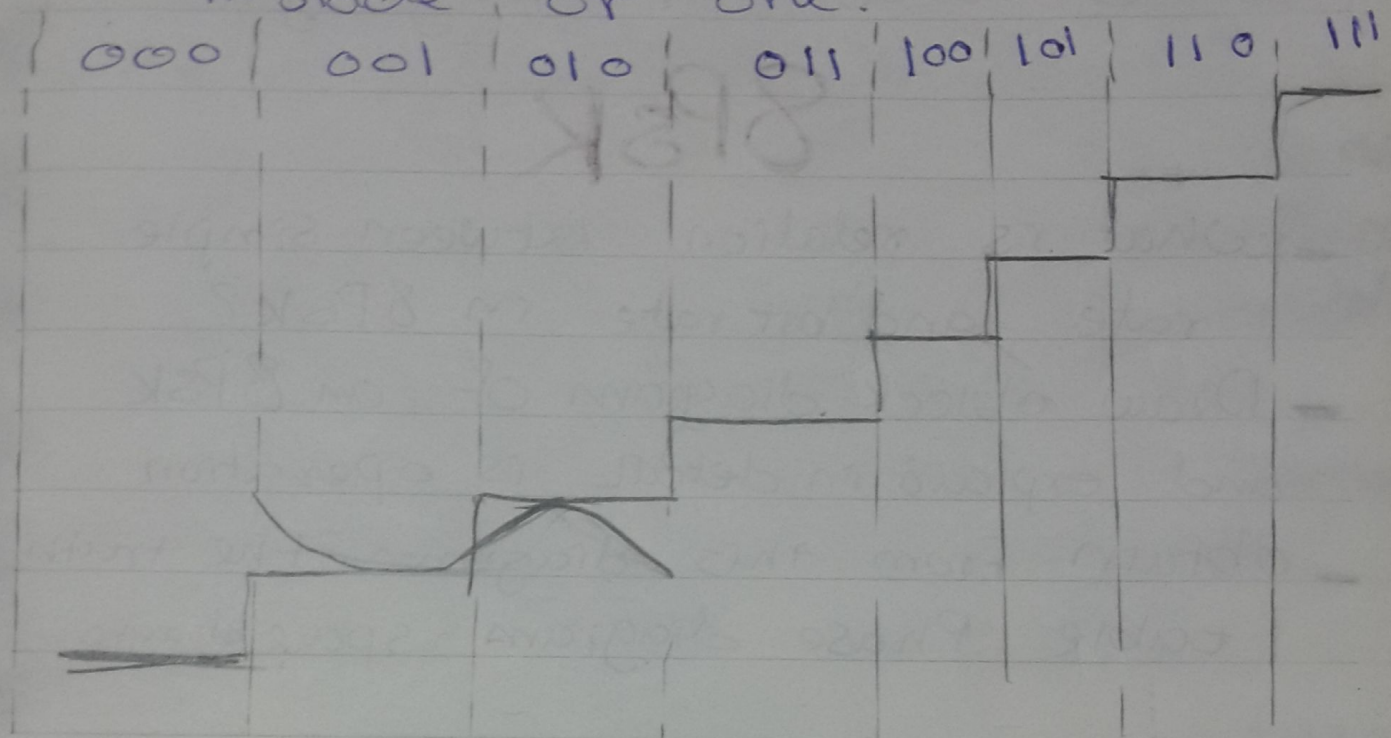
a digital sequence

$$I = 0, Q = 1, C = 1$$

with bit rate equal 5 Mbps
is transmitted using 8PSK if the
carrier Frequency $\sin \omega t = 120 \text{ MHz}$
determine the following:

- 1- The Baud rate
- 2- The BW of the modulator
- 3- The amplitude of the modulated carrier.
- 4- The phase of the output wave
- 5- sketch the wave form of O/P for 1 cycle.

we take 8 bits at the same time
in stack of one.



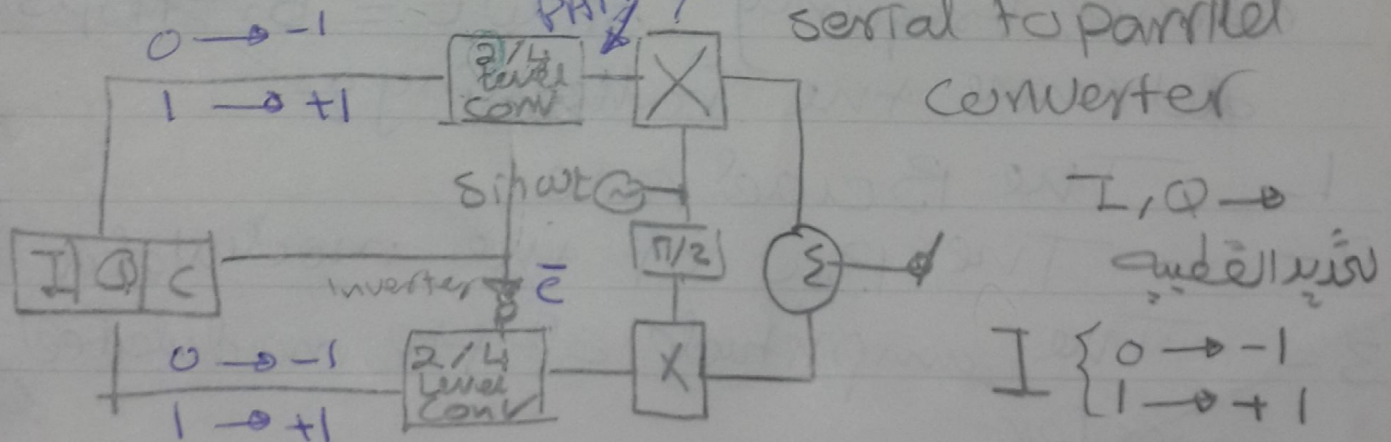
$T_{\text{eff}} =$

$$R_B = R_b / 3$$

$$f_{\text{eff}} = R_b / 2$$

$$f_{\text{eff}} = R_b / 6$$

block diagram by $\boxed{\text{I/Q}}$
balance modulator
PAM
serial to parallel converter



Control channel

0 \rightarrow 0-541
1 \rightarrow 1-307

$I \begin{cases} 0 \rightarrow -1 \\ 1 \rightarrow +1 \end{cases}$
 $Q \begin{cases} 0 \rightarrow -1 \\ 1 \rightarrow +1 \end{cases}$

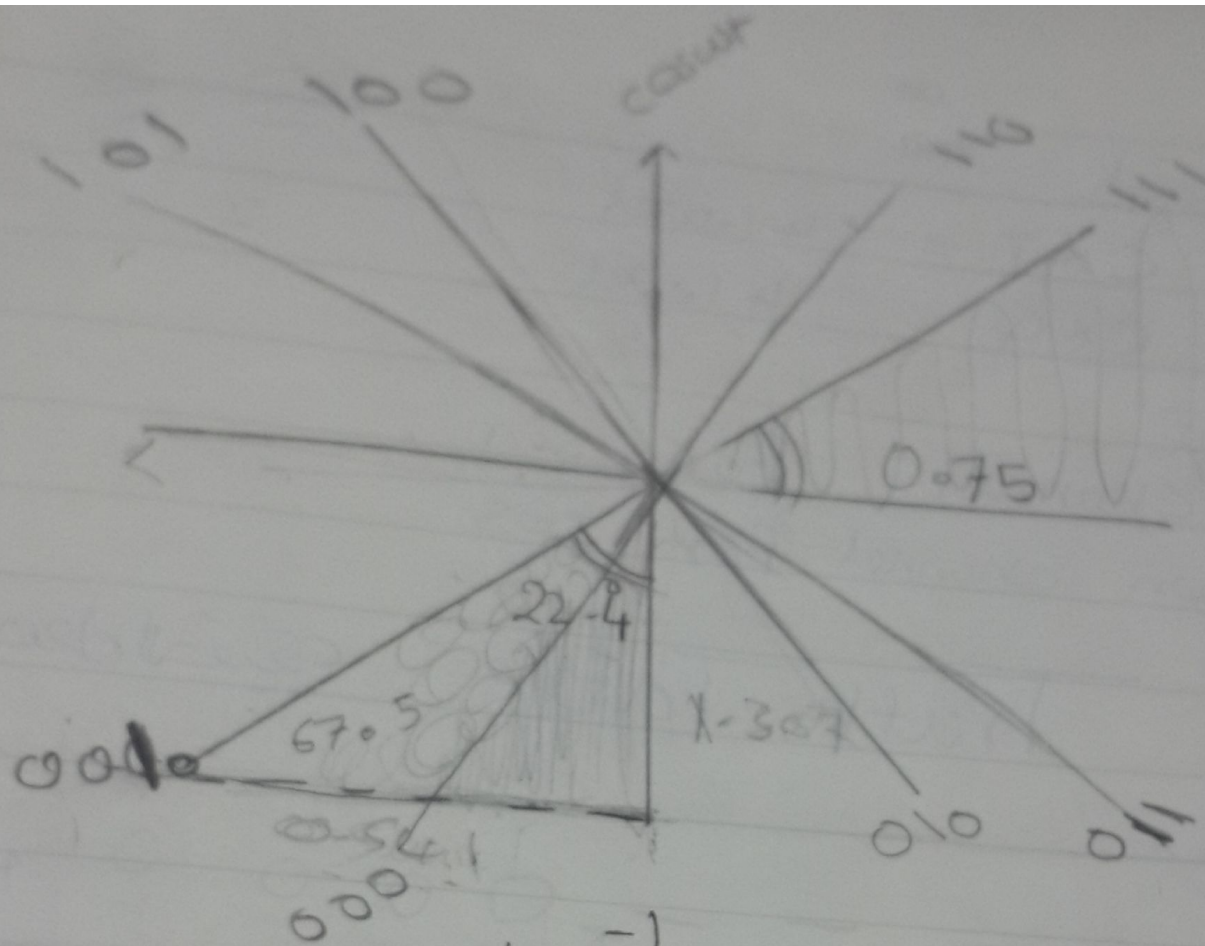
$C \times I = 4 \text{ level}$
 $C \times Q = 4 \text{ level}$

D/A
 2/4 cos

* up channel for sin
 * down channel for cos

Truth Table

I	Q	C	O/P
0	0	0	$+0.541 \sin - 1.307 \cos$
0	1	1	$1.307 \sin - 0.541 \cos$
1	0	1	$+1.307 \sin + 0.541 \cos$
1	1	0	$0.541 \sin + 1.307 \cos$
0	0	1	$1.307 \sin + 0.541 \cos$
0	1	0	$0.541 \sin - 1.307 \cos$
1	0	0	$-1.307 \sin + 0.541 \cos$
1	1	1	$-0.541 \sin - 1.307 \cos$



$$\tan^{-1} \frac{0.541}{1.307} = 22.4^\circ$$

$$22.4 + 90 = 112.4$$

$$1.41 \sin(\omega t - 112.5)$$

التي في الربع الثالث

$$0.75 \rightarrow 110$$

(8 Phasor should be draw)

IOC
001

$$-1.307 \sin \omega t - 0.541 \cos \omega t$$

the sum of any 2 sinusoids is
sinusoid $A \sin(\omega t + \phi)$

$$A \sin \omega t \cos \phi + A \sin \phi \cos \omega t$$

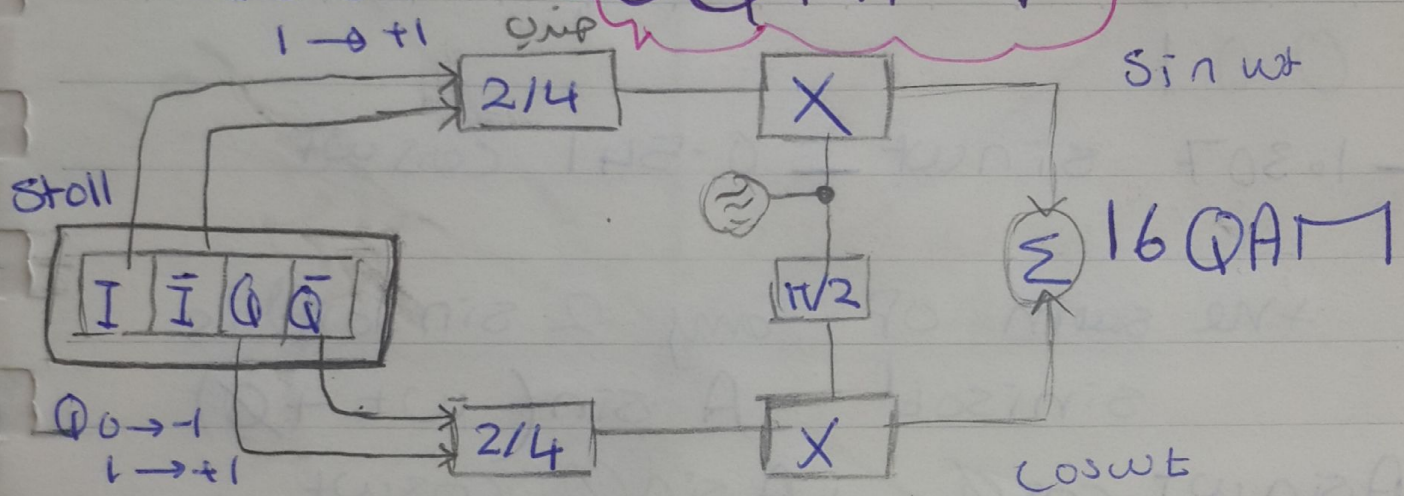
$$A \cos \phi = 1.307$$

$$A \sin \phi = 0.541$$

$$A = 1.41$$

$$A \sin \phi = -0.541$$

16QAM



* Change in Phase & Amplitude

0 → 0-220

1 → 0-821

* num of channels = 2 channels

* num of Phases = 12 Phases

Amp 30° phase

* we take 4 bits by serial to parallel converter (16 level)

* I → inphase channel
Carrier = sin wt

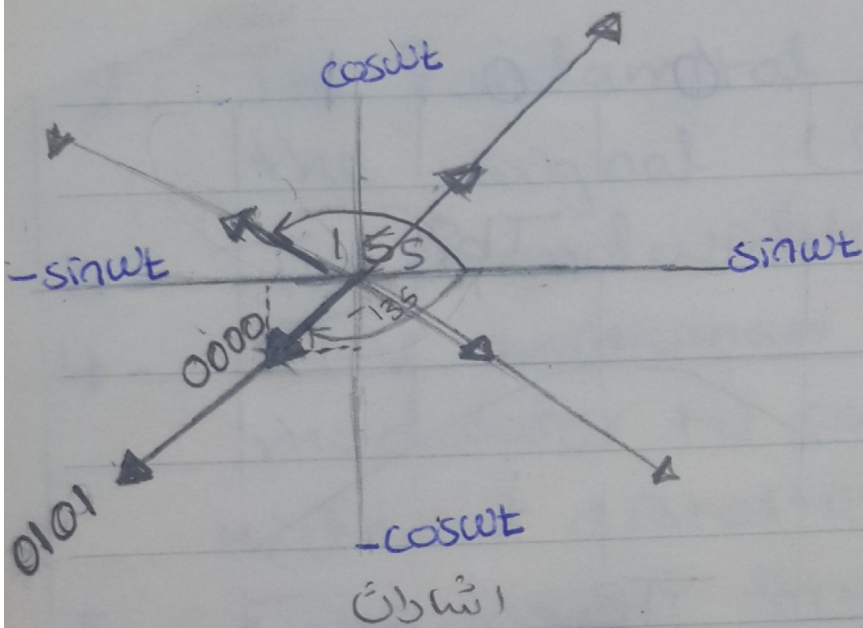
* I, Q → Polarity

I', Q' → control channel to change the level

Example:-

* 0000 → -0.220 sin wt - 0.220 cos wt

Q = -135°, A = 0.311, U = 0.311 sin(wt - 135°)



* $\underline{I} = 0, \bar{I} = 1, \underline{Q} = 0, \bar{Q} = 1$

$-0.821 \sin wt \quad -0.821 \cos wt$

$\phi = -135^\circ$

$A = 1.16 = \sqrt{(0.821)^2 + (0.821)^2}$

$U = 1.16 \sin(wt - 135^\circ)$

Example: Obtain the equation of the o/p of a 16 QAM if $\underline{I} = 0, \bar{I} = 1, \underline{Q} = 1, \bar{Q} = 0$

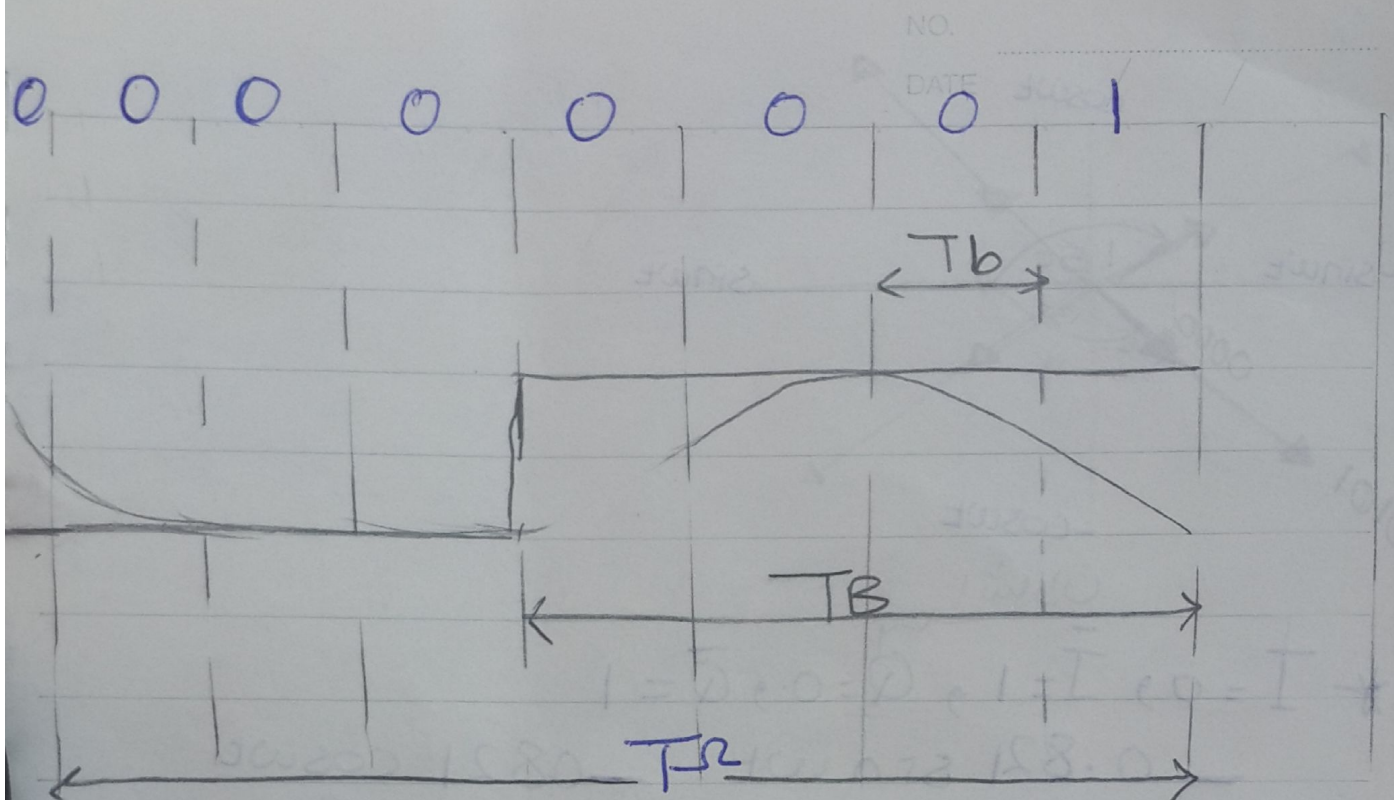
$-0.821 \sin wt + 0.220 \cos wt$

$\tan^{-1} \frac{0.821}{0.220} = 74.9^\circ + 90^\circ$

$= 165^\circ$

$A = 0.85 \sin(wt + 165^\circ)$

level \rightarrow 4 bit



$$R_B = R_b/4$$

$$F_R = R_b/8$$

$$B_W = R_b/4 = R_B$$

wave form
to the input
signal (original)

draw a block diagram of a 16 QAM modulator and obtain from it the truth table, the Phase diagram, the space diagram.

If the sequence is as follows

$I=1, \bar{I}=1, Q=0, \bar{Q}=1$ and the carrier frequency 120 MHz with bit rate 6 Mbps. Explain the structure and operation of the detector of the 16 QAM. also determine the following

1. The Band Rate

- NO. _____
DATE _____
- 2- The Fundamental Frequency of the signal (f_m)
 - 3- The Bandwidth of the signal
 - 4- The minimum BW of the modulator to carry the signal without distortion.
 - 5- The highest frequency after modulation
 - 6- write the equation of the output of the modulator
 - 7- Sketch the waveform of the o/p of the modulator.
- (notes: the carrier to be $\sin \omega t$)